

R7F100GLG2DFB#AA0

Data Sheet

Manufacturers

Renesas Technology Corp

Package/Case

Product Type

Embedded Processors & Controllers

Lifecycle

Images are for reference only

Please submit RFQ for R7F100GLG2DFB#AA0 or <u>Fmail to us: sales@ovaga.com</u> We will contact you in 12 hours.



General Description

Features

CPU: RL78 core, Max. 32MHz

Up to 768kB Flash Memory and 48kB SRAM

Memory: SRAM up to. 48KB, Program Flash up to 768KB

8KB Data Flash to store data as in EEPROM

Scalable from 30-pin to 128-pin packages

Wide operating voltage range of 1.6V to 5.5V

Enhanced Capacitive Sensing Unit (CTSU2L)

Timer: 16-bit Timer (ch) x 16, 8-bit Timer, Watchdog Timer (ch) x 1,32-bit Interval Timer x 1 ch

PWM: PWM Output x 14

Analog function: 12-bit A/D Converter (ch) x 26, 8-bit D/A - Converter (ch) x 2, Comparator x 2

On-chip Oscillator Freq. (MHz): High-Speed: up to 32MHz

Middle-Speed: up to 4MHz

Low-speed: 32.768kHz

Snooze mode sequencer

Logic & Event link controller

Others: RTC, Power-On Reset, Low Voltage Detection

A large selection of packages (LQFP, QFN, LGA, BGA, WLCSP)

Related Products



R7S721011VCBG#AC0

Renesas Technology Corp 324-BGA



R7S721021VCFP#AA1

Renesas Technology Corp 208-LQFP



R7S721001VCBG#AC1

Renesas Technology Corp 324-BGA



R7F7010233AFP#KA4

Renesas Technology Corp 100-LQFP



 $\underline{R7S721020VCBG\#AC1}$

Renesas Technology Corp 176-LFBGA



R7S721000VCFP#AA1

Renesas Technology Corp 256-LQFP

R7S721000VCFP#AA0



Renesas Technology Corp LQFP



R7S721001VCBG#AC0

Renesas Technology Corp 324-BGA