

XC5VLX30-1FFG676I

Data Sheet

FPGA Virtex®-5 LX Family 30720 Cells 65nm (CMOS) Technology 1V

Manufacturers <u>AMD Xilinx, Inc</u>

Package/Case BGA-676

Product Type Programmable Logic ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for XC5VLX30-1FFG676I or <u>Email to us: sales@ovaga.com</u> We will contact you in 12 hours.



General Description

XC5VLX30-1FFG676I is a Field Programmable Gate Array (FPGA) device from Xilinx, which is a programmable logic device that can be configured by the user after manufacturing. The XC5VLX30-1FFG676I is part of the Virtex-5 LX family, which is designed for high-performance applications.

Features

33,088 logic cells

640 Kb block RAM

12 Digital Clock Managers (DCMs)

8 Phase-Locked Loops (PLLs)

400 MHz maximum operating frequency

1.2V core voltage

1.5V/2.5V/3.3V auxiliary voltage options

676-pin Fine-pitch Ball Grid Array (FBGA) package



Related Products



XC18V01S020C

AMD Xilinx, Inc SOP-20



XCF04SV0G20C

AMD Xilinx, Inc TSSOP20



XC6SLX4-2CSG225C

AMD Xilinx, Inc BGA-225



XCV50-6BG256C

AMD Xilinx, Inc BGA256



XCF08PV0G48C

AMD Xilinx, Inc TSOP-48



XC6SLX25-3FTG256C

AMD Xilinx, Inc BGA-256



XC6SLX16-3CSG324C

AMD Xilinx, Inc BGA-324



XCF32PVO48C

AMD Xilinx, Inc TSOP48