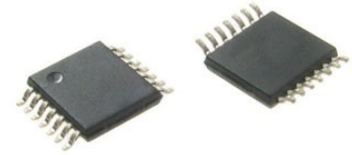


4-bit LVTTTL to GTL transceiver - Application: Processor Interface ; Function: Bi-Directional Non-Latched ; GTL Drive: 40 mA; Number of bits: 4 ; Operating temperature: -40~85 Cel; Supply voltage: 3.0~3.6 V; TTL Drive: 16 mA; Voltage Translation Range, Bus Transceivers
4-BIT BI-DIREC NON-LATCH TRAN

Manufacturers	NXP Semiconductor
Package/Case	TSSOP-14
Product Type	Bus Transceivers
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for GTL2014PW,118 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

Features

4-bit LVTTTL to GTL transceiver

Related Products



[GTL2014PW](#)

NXP Semiconductor
TSSOP-14



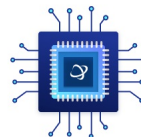
[GTL1655DGG](#)

NXP Semiconductor
SOT-646



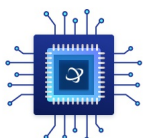
[GTL2002D](#)

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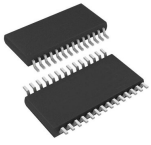
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