

1.5  $\Omega$  On Resistance,  $\pm 15$  V/12 V/ $\pm 5$  V, iCMOS®, Dual SPDT Switch; Package: TSSOP  
4.4 MM; No of Pins: 16; Temperature Range: TBD

Manufacturers	<a href="#">Analog Devices, Inc</a>
Package/Case	TSSOP-16
Product Type	Analog Switch ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADG1436YRUZ-REEL7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The ADG1436 is a monolithic CMOS device containing two independently selectable SPDT switches. An EN input on the LFCSP package enables or disable the device. When disabled, all channels are switched off. Each switch conducts equally well in both directions when on and has an input signal range that extends to the supplies. In the off condition, signal levels up to the supplies are blocked. Both switches exhibit break-before-makeswitching action for use in multiplexer applications.

The ADG1436 is designed on an iCMOS® process. iCMOS (industrial-CMOS) is a modular manufacturing process combining high voltage CMOS (complementary metal-oxide semiconductor) and bipolar technologies. It enables the development of a wide range of high performance analog ICs capable of 33 V operation in a footprint that no previous generation of high voltage parts has been able to achieve. Unlike analog ICs using conventional CMOS processes, iCMOS components can tolerate high supply voltages while providing increased performance, dramatically lower power consumption, and reduced package size.

The on-resistance profile is very flat over the full analog input range, ensuring excellent linearity and low distortion when switching audio signals. iCMOS construction ensures ultralow power dissipation, making the part ideally suited for portable and battery-powered instruments.

### Product Highlights

2.6  $\Omega$  maximum on resistance over temperature.

Minimum distortion.

Ultralow power dissipation:  $< 0.03 \mu\text{W}$ .

16-lead TSSOP and 16-lead 4 mm  $\times$  4 mm LFCSP packages.

## Features

1.5  $\Omega$  on resistance

0.3  $\Omega$  on-resistance flatness

0.1  $\Omega$  on-resistance match between channels

Continuous current per channel

LFCSP package: up to 400 mA

TSSOP package: up to 260 mA

Fully specified at +12 V,  $\pm 15$  V, and  $\pm 5$  V

No VL supply required

3 V logic-compatible inputs

Rail-to-rail operation

16-lead TSSOP and 4 mm  $\times$  4 mm, 16-lead LFCSP packages

ADG1436-EP supports defense and aerospace applications (AQEC standard)

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Military temperature range ( $-55^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ )

1 assembly/test site

Product change notification

Qualification data available on request

## Application

Automatic test equipment

Data acquisition systems

Battery-powered systems

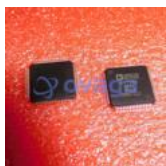
Sample-and-hold systems

Audio signal routing

Communication systems

Relay replacement

## Related Products



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