



Data Sheet

Operational Amplifier, Single, 1 Amplifier, 10 MHz, 50 V/ $\mu$ s,  $\pm$  8V to  $\pm$  20V, SOIC, 8 Pins

Manufacturers Analog Devices, Inc

Package/Case SOP8

Product Type Amplifier ICs

RoHS Rohs

Lifecycle

Please submit RFQ for OP42GSZ or Email to us: sales@ovaga.com We will contact you in 12 hours.



Images are for reference only

RFO

## **General Description**

Its tight  $750\mu V$  maximum input offset voltage combined with well-controlled drift of less than  $10\mu V/\% C$  eliminates the need for external nulling in many circuits. The common-mode rejection of the 86dB minimum over  $a\pm11~V$  input voltage range is exceptional for a high-speed amplifier. High CMR combined with a minimum 500V/mV gain into 10k(ohm) load ensure excellent linearity in both noninverting and inverting gain configurations. The low input bias and offset currents provided by the JFET input stage suit the OP42 for use in high-speed sample and hold circuits, peak detectors, and log amplifiers. Excellent radiation hardness characteristics make the OP42 ideal for military and aerospace applications.

The OP42 conforms to the standard 741 pinout with nulling to V-. The OP42 upgrades the performance of circuits using the AD544, AD611, AD711, and LF400 by direct replacement. In circuits without nulling, the OP42 offers an upgrade for designs using the OP16, OP17, LT1022, and LT1056.

## **Features**

**FAST** 

Slew Rate: 45 V/µs Min

Settling Time (0.01%): 1 µs Max

Gain Bandwidth Product: 10 MHz Typ

PRECISE

Common Mode Rejection: 86 dB Min

Open Loop Gain: 500 V/mV Min

Bias Current: 200 pA Max

Excellent Radiation Hardness

## **Related Products**



**OP213F** 

Analog Devices, Inc SMD/DIP-8/SOP-8



OP27GP

Analog Devices, Inc PDIP-8



OP462GSZ

Analog Devices, Inc SOIC-14



**OP467GPZ** 

Analog Devices, Inc PDIP-14



OP42AZ

Analog Devices, Inc CDIP-8



OP37GS

Analog Devices, Inc SOIC-8



**OP2177ARM** 

Analog Devices, Inc MSOP8



OP400GPZ

Analog Devices, Inc PDIP-14