

EPM7160SLC84-10N

Data Sheet

No. of Macrocells:160; No. of Pins:84; Operating Temp. Max:70 C; Operating Temp. Min:0 C; Propagation Delay:10ns RoHS Compliant: Yes;

Manufacturers	Altera Corporation (Intel)
Package/Case	PLCC-84
Product Type	Programmable Logic ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

\mathbf{N}	Please submit RFQ for EPM7160SLC84-10N or Email to us: sales@ovaga.com We will contact y	vou in 12 hours.	<u>RFO</u>
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General Description

EPM7160SLC84-10N is a programmable logic device (PLD) manufactured by Intel Corporation (formerly Altera). It belongs to the MAX 7000 series of PLDs and comes in a surface-mount package with 84 pins.

Features	Application
1600 usable gates	glue logic
60 macrocells (each can be configured as a combinatorial or registered logic element)	interface logic
10ns maximum propagation delay	bus interface control
5V operating voltage	simple state machines
ISP (in-system programmable) via JTAG interface	PAL replacement
	low-power designs



Related Products



EP4CE55F29C8N Altera Corporation (Intel)

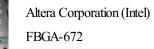
FBGA-780



Altera Corporation (Intel)

EPM1270T144A5N

TQFP-144 EP2C35F672C8N



EP2C35F484C7N

Altera Corporation (Intel) FBGA-484







Altera Corporation (Intel)

EPM7128AETC100-10

Altera Corporation (Intel) TQFP-100

EP2C35F484I8N



Altera Corporation (Intel) FBGA-484

EPM240M100C5N

Altera Corporation (Intel) BGA-100

EPM570F256C5N

FBGA-256