

Manufacturers	<u>AMD Xilinx, Inc</u>
Package/Case	BGA
Product Type	Integrated Circuits (ICs)
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XCV50BG256 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

RFQ

General Description

XCV50BG256 is a code that refers to a specific model of Xilinx FPGA (Field-Programmable Gate Array).

Features

It belongs to the Xilinx Virtex-II Pro family of FPGAs.

The "50" in its name refers to the number of thousands of logic cells it contains.

The "BG" refers to its package type, which is a 256-pin ball grid array (BGA).

It has a total of 52,160 logic cells and 1,296 kilobits of block RAM.

It supports a variety of I/O standards, including LVDS, RSDS, HSTL, SSTL, and more.

Application

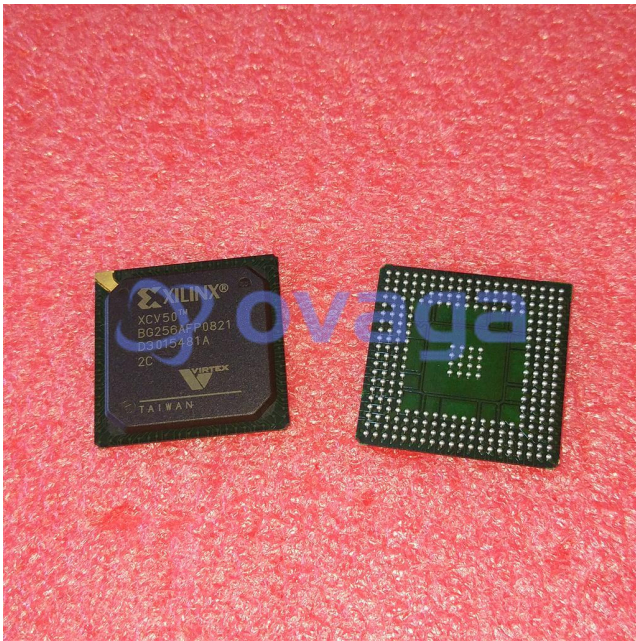
High-performance computing and data processing

Digital signal processing (DSP)

Industrial automation and control

Medical imaging and diagnostic equipment

Aerospace and defense systems



Related Products



[XCF01S](#)

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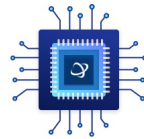
[XCF01SVOG20](#)

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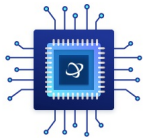
[XCD56366PV120](#)

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BGA



[XC18V00](#)

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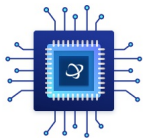
[XCF16P](#)

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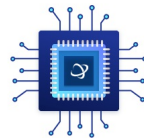
[XCF16PVOG48](#)

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[XCF02SV020](#)

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[XCF32PVOG48](#)

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