

# AD7980BRMZ

Data Sheet

16-Bit, 1 MSPS PulSAR® ADC in MSOP/QFN; Package: MSOP; No of Pins: 10; Temperature Range: Industrial

Manufacturers	Analog Devices, Inc
Package/Case	MSOP-10
Product Type	Data Conversion ICs
RoHS	Pb-free Halide free



Images are for reference only

Please submit RFQ for AD7980BRMZ or Email to us: sales@ovaga.com We will contact you in 12 hours.

<u>RFQ</u>

### **General Description**

The SPI-compatible serial interface also features the ability, using the SDI input, to daisy-chain several ADCs on a single, 3-wire bus and provides an optional busy indicator. It is compatible with 1.8 V, 2.5 V, 3 V, or 5 V logic, using the separate supply VIO.

The AD7980 is housed in a 10-lead MSOP or a 10-lead QFN (LFCSP) with operation specified from -40°C to +125°C.

Applications Battery-powered equipment

Communications

Lifecycle

ATE

Data acquisitions

Medical instruments

## Features

# Application

16-bit resolution with no missing codes	Battery-powered equipment
Throughput: 1 MSPS	Communications
Low power dissipation4 mW at 1 MSPS (VDD only)7 mW at 1 MSPS (total)70 µW at 10 kSPS	ATE
INL: $\pm 0.6$ LSB typical, $\pm 1.25$ LSB maximum	Data acquisitions
SINAD: 91.25 dB at 10 kHz	Medical instruments
THD: -110 dB at 10 kHz	
See data sheet for additional features	
AD7980-EP supports defense and aerospace applications (AQEC)	
Download(pdf)	
Controlled manufacturing baseline	
One assembly/test site	
One fabrication site	
Enhanced product change notification	
Qualification data available on request	
Military temperature range: $-55^{\circ}C$ to $+125^{\circ}C$	
V62/12640 DSCC Drawing Number	



#### **Related Products**



ADAS3022BCPZ Analog Devices, Inc LFCSP-40



LFCSP-40 AD574AJNZ

Analog Devices, Inc PDIP-28





AD7266BSUZ

Analog Devices, Inc TQPF-32

#### AD7401YRWZ

Analog Devices, Inc SOIC-16



#### AD7938BSUZ

Analog Devices, Inc TQFP-32



#### AD7192BRUZ-REEL

Analog Devices, Inc TSSOP-24



#### AD7124-8BCPZ-RL7

Analog Devices, Inc LFCSP-32



#### AD9680BCPZ-500

Analog Devices, Inc LFCSP-64