

MIC94310-GYMT-TR

Data Sheet

RFO

IC REG LDO 1.8V 0.2A 4TMLF

Manufacturers	Microchip Technology, Inc	
Package/Case	UDFN-4	- Contraction
Product Type	Power Management ICs	C.C.
RoHS		
Lifecycle		Images are for reference only

Please submit RFQ for MIC94310-GYMT-TR or Email to us: sales@ovaga.com We will contact you in 12 hours.

General Description

The MIC94310 Ripple BlockerTM is a monolithic integrated circuit that provides low-frequency ripple attenuation (switching noise rejection) to a regulated output voltage. This is important for applications where a DC/DC switching converter is required to lower or raise a battery voltage but where switching noise cannot be tolerated by sensitive downstream circuits such as in RF applications. The MIC94310 maintains high power supply ripple rejection (PSRR) with input voltages operating near the output voltage level to improve overall system efficiency. A low-voltage logic enable pin facilitates ON/OFF control at typical GPIO voltage levels.

The MIC94310 operates from an input voltage of 1.8V to 3.6V.

Packaged in a 4-pin 1.2mm x 1.6mm Thin DFN with a junction operating temperature range of -40°C to +125°C.

Features

1.8V to 3.6V input voltage range

Active noise rejection over a wide frequency band: >50dB from 10Hz to 10MHz at 200mA load

Rated to 200mA output current

Fixed output voltages

Current-limit and thermal-limit protected

1.2mm x 1.6mm, 4-pin Thin DFN

Logic-controlled enable pin

Intratica Fibratio

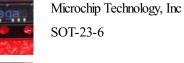
Ovaga Technologies Limited



MIC94325YMT-TR

Microchip Technology, Inc UDFN-6

MIC2009A-1YM6-TR





MIC5841YWM-TR Microchip Technology, Inc

SOIC-18



MIC29152WT Microchip Technology, Inc TO-220-5









MIC4684YM

Microchip Technology, Inc SOIC-8

MIC2090-1YM5-TR

Microchip Technology, Inc SOT-23-5

MIC5891YN

Microchip Technology, Inc PDIP-16

MIC5209YM

Microchip Technology, Inc SOIC-8