

ATSAML21J18B-AUT

Data Sheet

ATMEL ATSAML21J18B-AUT 32Bit Microcontroller, SMART SAML21, ARM Cortex-M0+, 48MHz, 256KB, 32KB, 64Pins, TQFP

| Manufacturers | Microchip Technology, Inc | |
|---------------------|--|--|
| Package/Case | TQFP-64 | Manager and Manager |
| Product Type | Embedded Processors & Controllers | and a state of the |
| RoHS | Green | |
| Lifecycle | | Images are for reference only |
| | | |
| Please submit RFQ f | for ATSAML21J18B-AUT or <u>Email to us: sales@ovaga.co</u> | m We will contact you in 12 hours. RFQ |

General Description

Theultra-low-power ATSAML21 based Flash microcontroller (MCU) featuresophisticated power management technologies and consumesunder $35 \,\mu$ A/MHz in active mode and 200nA in Sleep mode. In addition toultra-low-power performance, these devices feature Full Speed USB host and device, Event System and Sleepwalking, 12-bit analog, AES, capacitive touch sensing, built in opAmps and much more.

Supported by MPLAB X IDE and MPLAB Harmony.

Features

ARM Cortex-M0+ CPU running at up to 48MHz

256KB in-system self-programmable Flash

8KB Flash Read-While-Write section

32KB SRAM Main Memory

8KB SRAM Low power Memory

Operating voltage: 1.62V - 3.63V

Ultra Low Power Architecture consuming under 35uA/MHz in Active mode

Idle, Standby, Backup, and Off sleep modes

Battery backup support

Ovaga Technologies Limited

| Embedded Buck/LDO regulator supporting on-the-fly selection | | |
|---|--|--|
| Peripheral Touch Controller (PTC) | | |
| Wake-up on touch in standby mode | | |
| Two-pin Serial Wire Debug (SWD) programming, test and debugging interface | | |
| Temperature Range - 40°C to 85°C, - 40°C to 105°C | | |
| Peripheral Features: | | |
| 16-channel Direct Memory Access Controller (DMAC) | | |
| 12-channel Event System | | |
| 16-bit Timer/Counters (TC) including one low-power TC | | |
| Two 24-bit and one 16-bit Timer/Counters for Control (TCC) | | |
| 32-bit Real Time Counter (RTC) with clock/calendar function | | |
| Watchdog Timer (WDT) | | |
| CRC-32 generator | | |
| One full-speed (12Mbps) Universal Serial Bus (USB) 2.0 interface | | |
| Up to six Serial Communication Interfaces (SERCOM) including one low-power SERCOM | | |
| USART, I2C, SPI, LIN slave | | |
| One AES Encryption Engine & TRNG | | |
| One Configurable Custom Logic (CCL) | | |
| Advanced Analog: | | |
| One 12-bit, 1MSPS Analog-to-Digital Converter (ADC) | | |
| Two channel, 12-bit, 1MSPS Dual Output Digital-to-Analog Converter (DAC) | | |
| Two Analog Comparators (AC) with window compare function | | |
| Three Operational Amplifiers (OPAMP) | | |
| | | |

Related Products



ATSAMA5D36A-CU

Microchip Technology, Inc LFBGA-324



ATMEGA32M1-AU

Microchip Technology, Inc TQFP-32



ATXMEGA128D3-AU

Microchip Technology, Inc TQFP-64



ATMEGA64M1-15AZ

Microchip Technology, Inc TQFP-32



ATTINY48-MU

Microchip Technology, Inc VQFN-32







ATTINY2313V-10SU

Microchip Technology, Inc SOIC-20

ATMEGA16L-8PU

Microchip Technology, Inc PDIP-40

ATTINY4-TSHR

Microchip Technology, Inc SOT-23-6