

# AD7731BRZ

Data Sheet

Low Noise, High Throughput 24-Bit Sigma-Delta ADC

Manufacturers	Analog Devices, Inc
Package/Case	SOIC24
Product Type	Data Conversion ICs
RoHS	Pb-free Halide free
Lifecvcle	



Images are for reference only

Please submit RFQ for AD7731BRZ or Email to us: sales@ovaga.com We will contact you in 12 hours.

<u>RFQ</u>

### **General Description**

The AD7731 is a complete analog front-end for process controlapplications. The device has a proprietary programmable gainfront end that allows it to accept a range of input signal ranges, including low level signals, directly from a transducer. The signadeltaarchitecture of the part consists of an analog modulatorand a low pass programmable digital filter, allowing adjustment of filter cutoff, output rate and settling time.

The part features three buffered differential programmable gainanalog inputs (which can be configured as five pseudo-differentialinputs), as well as a differential reference input. The part operates from a single +5 V supply and accepts seven unipolar analoginput ranges: 0 to +20 mV, +40 mV, +80 mV, +160 mV, +320 mV, +640 mV and +1.28 V, and seven bipolar ranges  $\pm 20 \text{ mV}$ ,  $\pm 40 \text{ mV}$ ,  $\pm 80 \text{ mV}$ ,  $\pm 160 \text{ mV}$ ,  $\pm 320 \text{ mV}$ ,  $\pm 640 \text{ mV}$  and  $\pm 1.28 \text{ V}$ , and seven bipolar ranges: 16 bits at an 800 Hz output rate. The part can switchbetween channels with 1 ms settling time and maintain a performancelevel of 13 bits of peak-to-peak resolution.

The serial interface on the part can be configured for three-wireoperation and is compatible with microcontrollers and digital processors. The AD7731 contains self-calibration and system calibration options and features an offset drift of less than 5 nV/°C and a gain drift of less than 2 ppm/°C.

The part is available in a 24-lead plastic DIP, a 24-lead SOIC and 24-lead TSSOP package.

## Features

- 24-Bit Sigma-Delta ADC
- 16 Bits p-p Resolution at 800 Hz Output Rate
- Programmable Output Rates up to 6.4 kHz
- Programmable Gain Front End
- **Buffered Differential Inputs**
- Programmable Filter Cutoffs
- FASTStep<sup>TM\*</sup> Mode for Channel Sequencing
- Single Supply Operation

# Application

Process Control

PLCs/DCS

Industrial Instrumentation



#### **Related Products**



Analog Devices, Inc LFCSP-40

ADAS3022BCPZ



LFCSP-40 AD574AJNZ

Analog Devices, Inc PDIP-28





AD7266BSUZ

Analog Devices, Inc TQPF-32

#### AD7401YRWZ

Analog Devices, Inc SOIC-16



#### AD7938BSUZ

Analog Devices, Inc TQFP-32



#### AD7192BRUZ-REEL

Analog Devices, Inc TSSOP-24



#### AD7124-8BCPZ-RL7

Analog Devices, Inc LFCSP-32



#### AD9680BCPZ-500

Analog Devices, Inc LFCSP-64