

7201LA50TDB

Data Sheet

IC MEM FIFO 512X9 50NS 28TCERDIP; HT SUSA CODE:8542320070

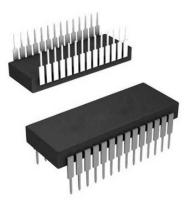
Manufacturers Renesas Technology Corp

Package/Case CDIP28

Product Type FIFO

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for 7201LA50TDB or <u>Emailto:sales@ovaga.com</u> We will contact you in 12 hours.

RFO

General Description

Features

Pin and functionally compatible with 720X family

Low power consumption

Ultra high speed—12ns access time

Asynchronous and simultaneous read and write

Fully expandable by both word depth and/or bit width

Status Flags: Empty, Half-Full, Full

Auto-retransmit capability

MIL-STD-883, Class B available in 28 pin (300 & 600 mil) Cerdip package

Industrial temperature range (-40C to +85C) is available

Available in 28 pin SOIC, PDIP (300 & 600 mil), and 32 pin PLCC packages

Related Products



7205L15J

Renesas Technology Corp PLC32



7204L25JI

Renesas Technology Corp 32-LCC (J-Lead)



72V36110L10PF

Renesas Technology Corp TQFP-100



72V2113L10PFI

Renesas Technology Corp TQFP-80



7204L12J

Renesas Technology Corp 32-LCC (J-Lead)



72V275L15PF

Renesas Technology Corp TQFP-64



72V261LA10TFG

Renesas Technology Corp 64-LQFP



7207L20DB

Renesas Technology Corp CDIP-28