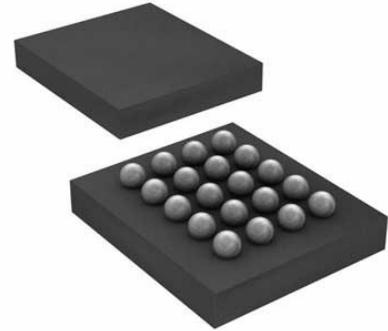


DSP Fixed-Point 16bit 500MHz 208-Pin CSP-BGA Tray

Manufacturers	Analog Devices, Inc
Package/Case	BGA-208
Product Type	Embedded Processors & Controllers
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for ADSP-BF537BBCZ-5B or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The Blackfin® Processor family has been expanded to address the ever-increasing need for pervasive embedded network connectivity with two new family members. This connectivity is powerful when utilized in conjunction with the high performance 16-/32-bit Blackfin embedded processor core, the flexible cache architecture, the enhanced DMA subsystem, and the Dynamic Power Management (DPM) functionality. System designers can take advantage of the combined control and signal processing capabilities of the processor core across a wide range of end applications through the scalability of the pin and code compatibility of these new family members.

The ADSP-BF536 and ADSP-BF537 are a functional extension of the popular ADSP-BF531/ADSP-BF532/ADSP-BF533 processors, and they are ideally suited for a variety of networked applications. The ADSP-BF537 is the higher performance series member, with more embedded memory enabling higher throughput needs for embedded applications such as video security/surveillance and industrial-environment-based distributed control/factory automation applications. The ADSP-BF536 offers exceptional performance and is designed for low cost connected devices such as remote monitoring devices, VoIP, point-of-sale terminals, and biometrics/security applications. Both devices are ideally suited for a broad range of industrial, instrumentation, medical, and consumer appliance applications—allowing for scalability based upon the required network bandwidth and mix of control, plus signal processing needed in the end product.

Features

Up to 600MHz High Performance Blackfin processor

132KB of on chip full speed SRAM

10-stage RISC MCU/DSP pipeline with mixed 16-/32-bit ISA for optimal code density

Powerful and flexible cache architecture suitable for soft real-time control tasks and industry-standard operating systems, plus hard real-time signal processing

Full SIMD architecture, including instructions for accelerated video and image processing

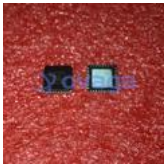
Embedded IEEE 802.3 Compliant 10/100 Ethernet MAC with buffered oscillator output to a separate PHY

Controller Area Network (CAN) 2.0B interface

Two-Wire Interface Controller

Glueless Video Capture/Display Port

Related Products



[ADUC7022BCPZ62](#)

Analog Devices, Inc
LFCSP-40



[ADUC7020BCPZ62](#)

Analog Devices, Inc
LFCSP-40



[ADUC841BSZ62-5](#)

Analog Devices, Inc
QFP-52



[ADUC841BSZ62-3](#)

Analog Devices, Inc
QFP-52



[ADUC831BSZ](#)

Analog Devices, Inc
QFP-52



[ADSP-BF527BBCZ-5A](#)

Analog Devices, Inc
BGA-208



[ADSP-21369BBPZ-2A](#)

Analog Devices, Inc
SBGA-256



[ADSP-BF561SBBCZ-5A](#)

Analog Devices, Inc
CSPBGA-256