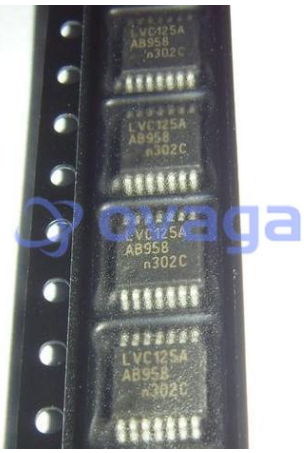
74LVC125APW

Data Sheet

Quad buffer/line driver with 5-volt tolerant inputs/outputs 3-State

Manufacturers	NXP Semiconductor
Package/Case	TSSOP-14
Product Type	Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 74LVC125APW or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

74LVC125APW is a quad buffer/line driver with 3-state outputs, which is a type of digital logic IC (integrated circuit). The "74" in the name indicates that it is part of the 74 series of logic chips.

Features

Four independent buffer/line drivers in one package

3-state outputs for bus-oriented applications

5V tolerant inputs when operating at 3.3V supply voltage

Low power consumption

High noise immunity

Wide operating voltage range: 1.65V to 5.5V

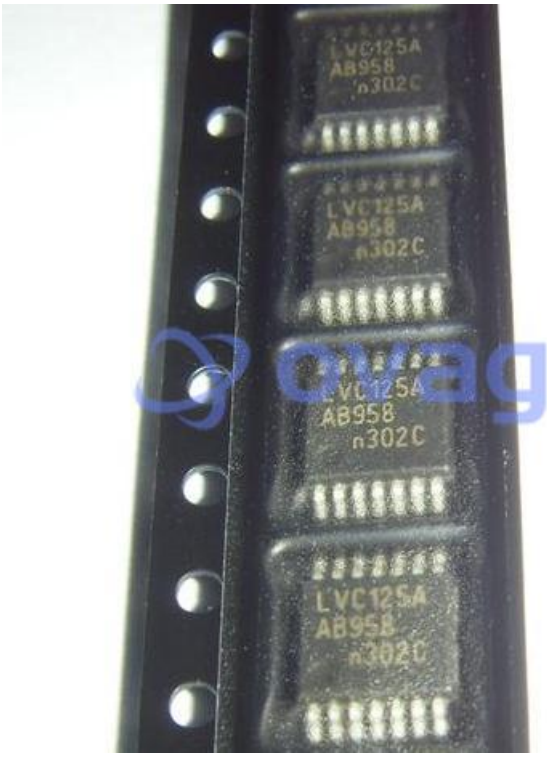
Application

Level translation between different voltage domains

Driving LEDs and other low-power loads

Interface between a microcontroller and other digital circuits

Bus drivers for I2C, SPI, and other communication protocols


LVC125A
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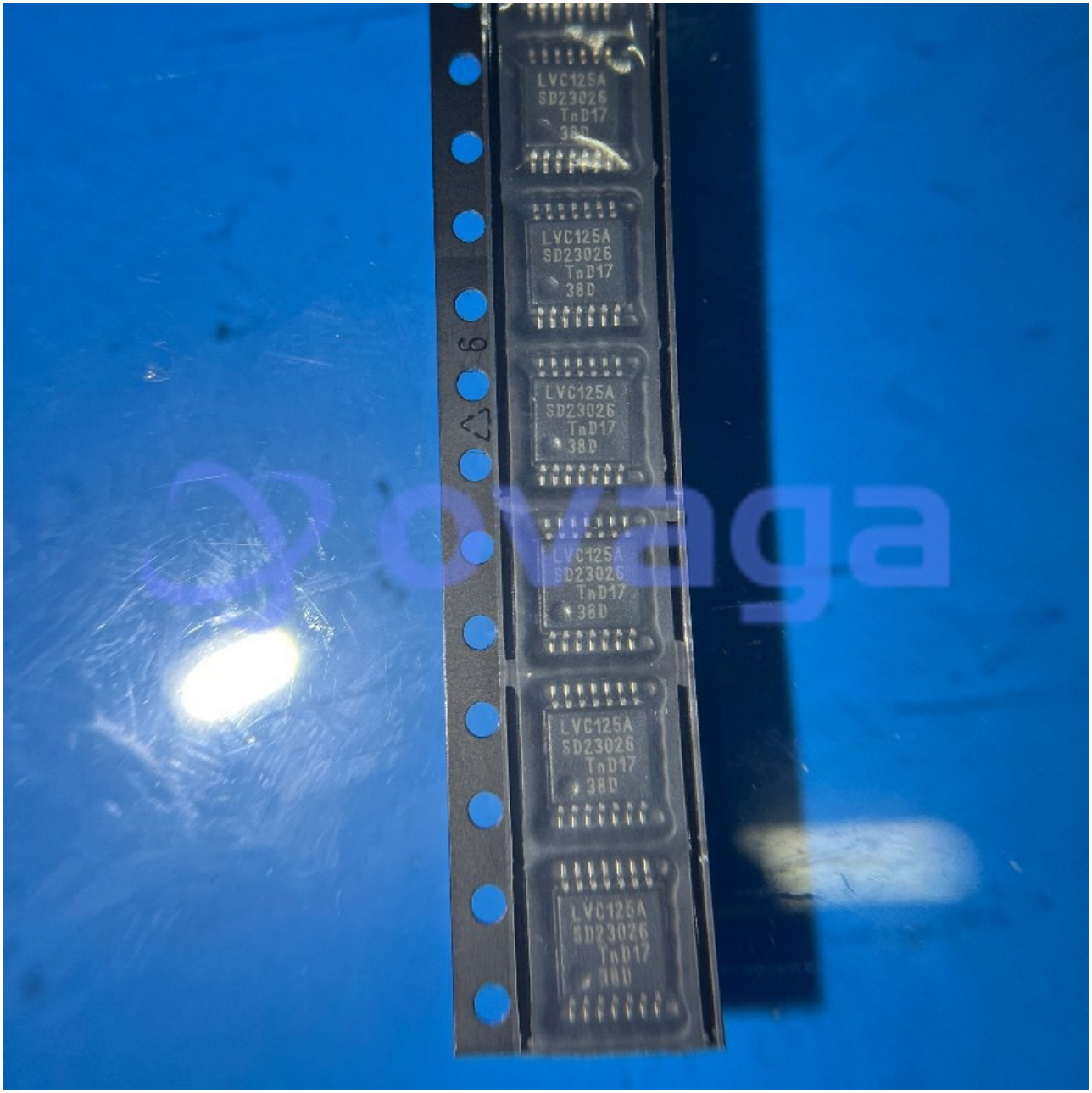

Related Products

[74HC4050D](#)

NXP Semiconductor

16-SOIC

[74HC574D](#)

NXP Semiconductor

20-SOIC

[74HC132D](#)

NXP Semiconductor

SOP-14

[74HC165D](#)

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[74HCT02D](#)

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SOP-14

[74HC14D]

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[74HC04D]

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SOP-14