

# **9ZXL1251EKILFT**

Data Sheet

12-Output DB1200ZL PCIe ClockClock Buffer

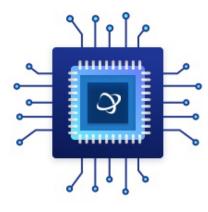
Manufacturers Renesas Technology Corp

Package/Case

Product Type Clock & Timer ICs

**RoHS** 

Lifecycle



Images are for reference only

Please submit RFQ for 9ZXL1251EKILFT or Email to us: sales@ovaga.com We will contact you in 12 hours.



# **General Description**

The 9ZXL1251E is a second-generation, enhanced performance DB1200ZL differential buffer. The partis apin-compatible upgradeto the 9ZXL1251A, while offering much improved phase jitter performance and increased system security features. A fixed external feedback maintains low drift for critical QPI/UPI applications.

### **Features**

LP-HCSL outputs with  $85\Omega$  Zout; eliminate 48 resistors, save  $82\text{mm}^2$  of area

PCIe Gen 1-5 compliance

12 OE# pins; hardware control of each output

9 selectable SMBus addresses; multiple devices can share the same SMBus segment

Selectable PLL BW; minimizes jitter peaking in cascaded PLL topologies

Hardware/SMBus control of PLL bandwidth and bypass; change mode without power cycle

Spread spectrum compatible; tracks spreading input clock for EMI reduction

100MHz and 133.33MHz PLL Mode; UPI and legacy QPI support

9 x 9 mm 64-VFQFPN package; small board footprint

#### Dalated Draduets



## 9ZX21901CKLF

Renesas Technology Corp 72-VFQFN



## 9ZXL0451EKILFT

Renesas Technology Corp



# **9ZXL1950BKLF**

Renesas Technology Corp 72-VFQFN



### **9ZXL1930BKLF**

Renesas Technology Corp 72-VFQFN



## 9ZXL0851EKILFT

Renesas Technology Corp



# 9ZXL1950BKLFT

Renesas Technology Corp



## 9ZXL1930BKLFT

Renesas Technology Corp 72-VFQFN



### **9ZXL1550BKLF**

Renesas Technology Corp