

XC3S5000-4FGG900I

Data Sheet

5M Gates 74880 Cells 630MHz 90nm Technology 1.2V

Manufacturers <u>AMD Xilinx, Inc</u>

Package/Case BGA-900

Product Type Programmable Logic ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for XC3S5000-4FGG900I or Emailto:si:sales@ovaga.com We will contact you in 12 hours.



General Description

XC3S5000-4FGG900I is a Field-Programmable Gate Array (FPGA) from Xilinx. It is a member of the Spartan-3 family of FPGAs and has 5000K logic cells. Here are some of its features:

Features Application

Number of logic cells: 5000K Digital signal processing

Number of I/O pins: 432 Image processing

Clock management tiles: 8 Communications

Digital Signal Processing (DSP) slices: 352 High-performance computing

Block RAM: 4.3 Mb Industrial control systems

Maximum operating frequency: 625 MHz

Aerospace and defense

Package: FG900 Video processing and display



Related Products



XC18V01S020C

AMD Xilinx, Inc SOP-20



XCF04SV0G20C

AMD Xilinx, Inc TSSOP20



XC6SLX4-2CSG225C

AMD Xilinx, Inc BGA-225



XCV50-6BG256C

AMD Xilinx, Inc BGA256



XCF08PV0G48C

AMD Xilinx, Inc TSOP-48



XC6SLX25-3FTG256C

AMD Xilinx, Inc BGA-256



XC6SLX16-3CSG324C

AMD Xilinx, Inc BGA-324



XCF32PVO48C

AMD Xilinx, Inc TSOP48