

FPGA Spartan-II Family 200K Gates 5292 Cells 263MHz 0.18um Technology 2.5V 208Pin PQFP

Manufacturers	AMD Xilinx, Inc
Package/Case	208-BFQFP
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC2S200-5PQ208C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC2S200-5PQ208C is a field-programmable gate array (FPGA) from the Xilinx Spartan-II series.

Features

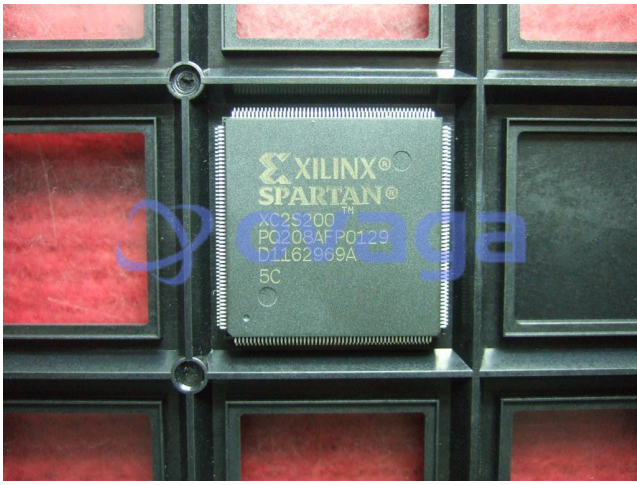
It has a capacity of 200,000 system gates and 392 user I/Os.

It operates at a maximum frequency of 200MHz.

It has a 5ns minimum clock-to-output delay.

It has 1,152 Kbits of block RAM and 120 programmable I/Os.

It can be programmed and reprogrammed in-circuit.



Related Products



[XC18V01S020C](#)

AMD Xilinx, Inc
SOP-20



[XCF04SV0G20C](#)

AMD Xilinx, Inc
TSSOP20



[XC6SLX4-2CSG225C](#)

AMD Xilinx, Inc
BGA-225



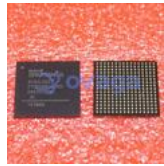
[XCV50-6BG256C](#)

AMD Xilinx, Inc
BGA256



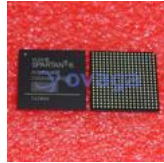
[XCF08PV0G48C](#)

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[XC6SLX25-3FTG256C](#)

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[XC6SLX16-3CSG324C](#)

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