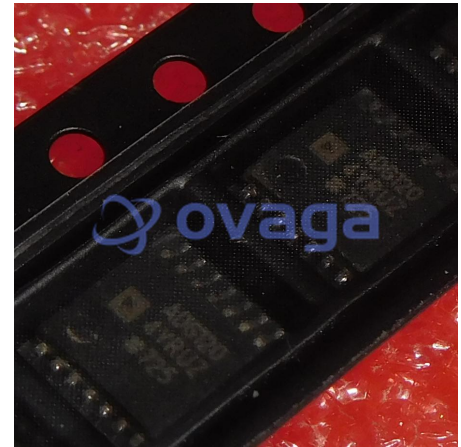


4:1 Analog Multiplexer IC, Single, 120 ohm, 10.8V to 13.2V, TSSOP-14

Manufacturers	Analog Devices, Inc
Package/Case	TSSOP-14
Product Type	Interface - Switches, Multiplexers, Demultiplexers
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADG1204YRUZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADG1204 is a complementary metal-oxide semiconductor (CMOS) analog multiplexer, comprising four single channels designed on an iCMOS (industrial CMOS) process. iCMOS® is a modular manufacturing process that combines high voltage CMOS and bipolar technologies. It enables the development of a wider range of high performance analog ICs capable of 33 V operation in a footprint that no previous generation of high voltage devices has been able to achieve. Unlike analog ICs using conventional CMOS processes, iCMOS components can tolerate high supply voltages while providing increased performance, dramatically lower power consumption, and reduced package size.

The ultralow capacitance and charge injection of this multiplexer makes it an ideal solution for data acquisition and sample-and-hold applications, where low glitch and fast settling are required. Fast switching speed coupled with high signal bandwidth makes the device suitable for video signal switching. iCMOS construction ensures ultralow power dissipation, making the device ideally suited for portable and battery-powered instruments.

The ADG1204 switches one of four inputs to a common output, D, as determined by the 3-bit binary address lines: A0, A1, and EN. Logic 0 on the EN pin disables the device. Each switch conducts equally well in both directions when on and has an input signal range that extends to the supplies. In the off condition, signal levels up to the supplies are blocked. All switches exhibit break-before-make switching action.

Product Highlights

1. 1.5 pF off capacitance (± 15 V supply).

<1 pC charge injection.

3 V logic-compatible digital inputs: = 0.8 V.

No VL logic power supply required.

Ultralow power dissipation: <0.03 μ W.

14-lead TSSOP and 12-lead, 3 mm \times 3 mm LFCSP packages.

Features

1.5 pF off source capacitance

33 V supply range

120 Ω on resistance

Fully specified at ± 15 V, +12 V

No VL supply required

3 V logic-compatible inputs

Rail-to-rail operation

14-lead TSSOP and 12-lead LFCSP

Typical power consumption $< 0.03 \mu\text{W}$

Application

Automatic test equipment

Data acquisition systems

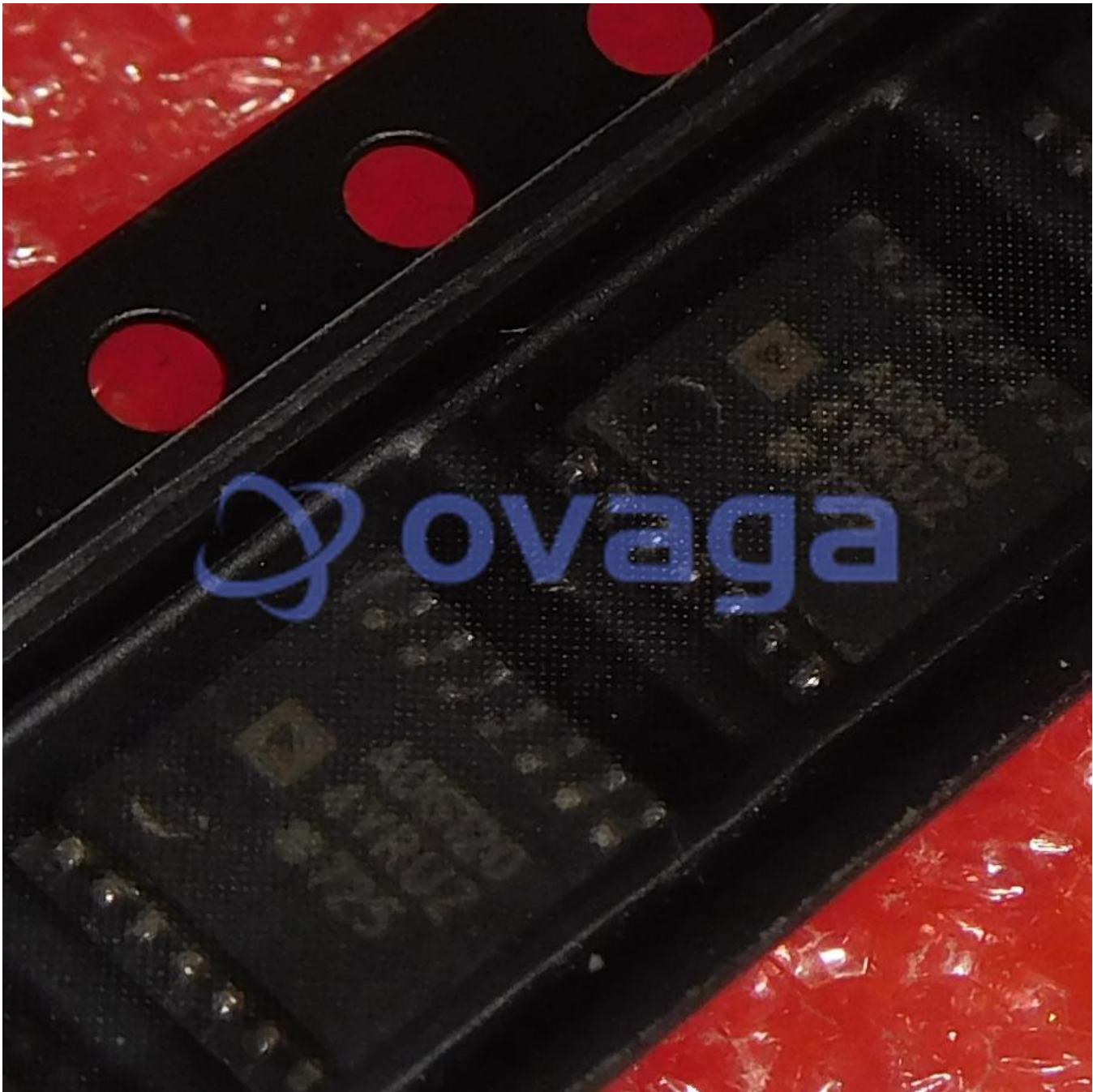
Battery-powered systems

Sample-and-hold systems

Audio signal routing

Video signal routing

Communication systems



Related Products



[ADV7181CBSTZ](#)

Analog Devices, Inc
LQFP-64



[AD8170AR](#)

Analog Devices, Inc
SOP8



[AD724JR](#)

Analog Devices, Inc
SOIC-16



[ADV7393BCPZ](#)

Analog Devices, Inc
LFCSP-VQ-40



[ADV7391WBCPZ](#)

Analog Devices, Inc
LFSCP-3



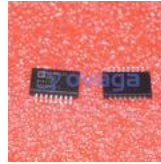
[ADV7390BCPZ](#)

Analog Devices, Inc
QFN32



[ADV7341BSTZ](#)

Analog Devices, Inc
LQFP-64



[ADUM4160BRIZ](#)

Analog Devices, Inc
SOIC-16