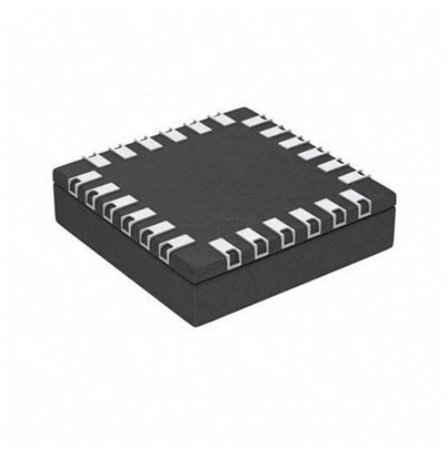
ADP1764ACPZ-R7

Data Sheet

LDO Regulator Pos 0.5V to 1.5V 4A 16-Pin LFCSP EP T/R

Manufacturers	Analog Devices, Inc
Package/Case	16-WFQFN, CSP
Product Type	Power Management ICs
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for ADP1764ACPZ-R7 or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

The ADP1764 is a low noise, low dropout (LDO) linear regulator. It is designed to operate from a single input supply with an input voltage as low as 1.10 V without the requirement of an external bias supply to increase efficiency and provide up to 4 A of output current (IOUT).

The low 47 mV typical dropout voltage at a 4 A load allows theADP1764 to operate with a small headroom while maintainingregulation and providing better efficiency.

The ADP1764 is optimized for stable operation with small 22 µF ceramic output capacitors. The ADP1764 delivers optimal transientperformance with minimal printed circuit board (PCB) area.

The ADP1764 is available in fixed output voltages ranging from 0.55 V to 1.5 V. The output voltage (VOUT) of the adjustableoutput model can be set from 0.5 V to 1.5 V through an external resistor connected between VADJ and ground.

The ADP1764 has an externally programmable soft start time by connecting a capacitor to the SS pin. Short-circuit and thermaloverload protection circuits prevent damage in adverse conditions.The ADP1764 is available in a small, 16-lead LFCSP package forthe smallest footprint solution to meet a variety of applications.

Features

4 A maximum output current

Low input voltage supply>

Fixed output voltage range (VOUT_FIXED): 0.55 V to 1.5 V

Adjustable output voltage range (VOUT_ADJ): 0.5 V to 1.5 V

Application

Regulation to noise sensitive applications such as radio frequency (RF) transceivers, analog-to-digital converter (ADC) and digital-to-analog converter (DAC) circuits, phase-locked loops (PLLs), voltage controlled oscillators (VCOs) and clocking integrated circuits

Field-programmable gate array (FPGA) and digital signal processor (DSP) supplies

Medical and healthcare

Industrial and instrumentation

Ultralow noise: 2 µV rms, 100 Hz to 100 kHz

Noise spectral density: 5 nV/√Hz at 10 kHz; 4 nV/√Hz at 100 kHz

Low dropout voltage: 47 mV typical at 4 A load

Operating supply current: 5 mA typical at no load

Excellent power supply rejection ratio (PSRR) performance

69 dB typical at 10 kHz at 4 A load

46 dB typical at 100 kHz at 4 A load

Excellent load/line transient response

Soft start to reduce inrush current

Optimized for small 22 µF ceramic capacitors

Current-limit and thermal overload protection

Power-good indicator

Precision enable

16-lead, 3 mm × 3 mm LFCSP package

Related Products

ADP3336ARMZ-REEL7

Analog Devices, Inc

MSOP-8

AD737JRZ

Analog Devices, Inc

SOP-8

[ADP3367ARZ]

Analog Devices, Inc

SOIC-8

[AD636JH]

Analog Devices, Inc

TO-100-10

[ADP3330ARTZ3.3-RL7]

Analog Devices, Inc

SOT-23-6

[ADR434BRZ]

Analog Devices, Inc

SOIC-8

[ADR421ARZ]

Analog Devices, Inc

SOP-8

[ADR3412ARJZ-R7]

Analog Devices, Inc

SOT-23-6