EPM7032AETI44-7N

Data Sheet

CPLD MAX 7000A Family 600 Gates 32 Macro Cells 138.9MHz CMOS Technology 3.3V

Manufacturers	Altera Corporation (Intel)
Package/Case	TQFP-44
Product Type	Programmable Logic ICs
RoHS	Rohs
Lifecycle	

Images are for reference only

Please submit RFQ for EPM7032AETI44-7N or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

EPM7032AETI44-7N is a type of programmable logic device (PLD) manufactured by Intel Corporation (formerly Altera Corporation), now part of Intel PSG.

Features

It has 32 macrocells (equivalent to logic blocks or flip-flops) and 32 inputs/outputs (I/Os).

It operates at a maximum frequency of 133 MHz.

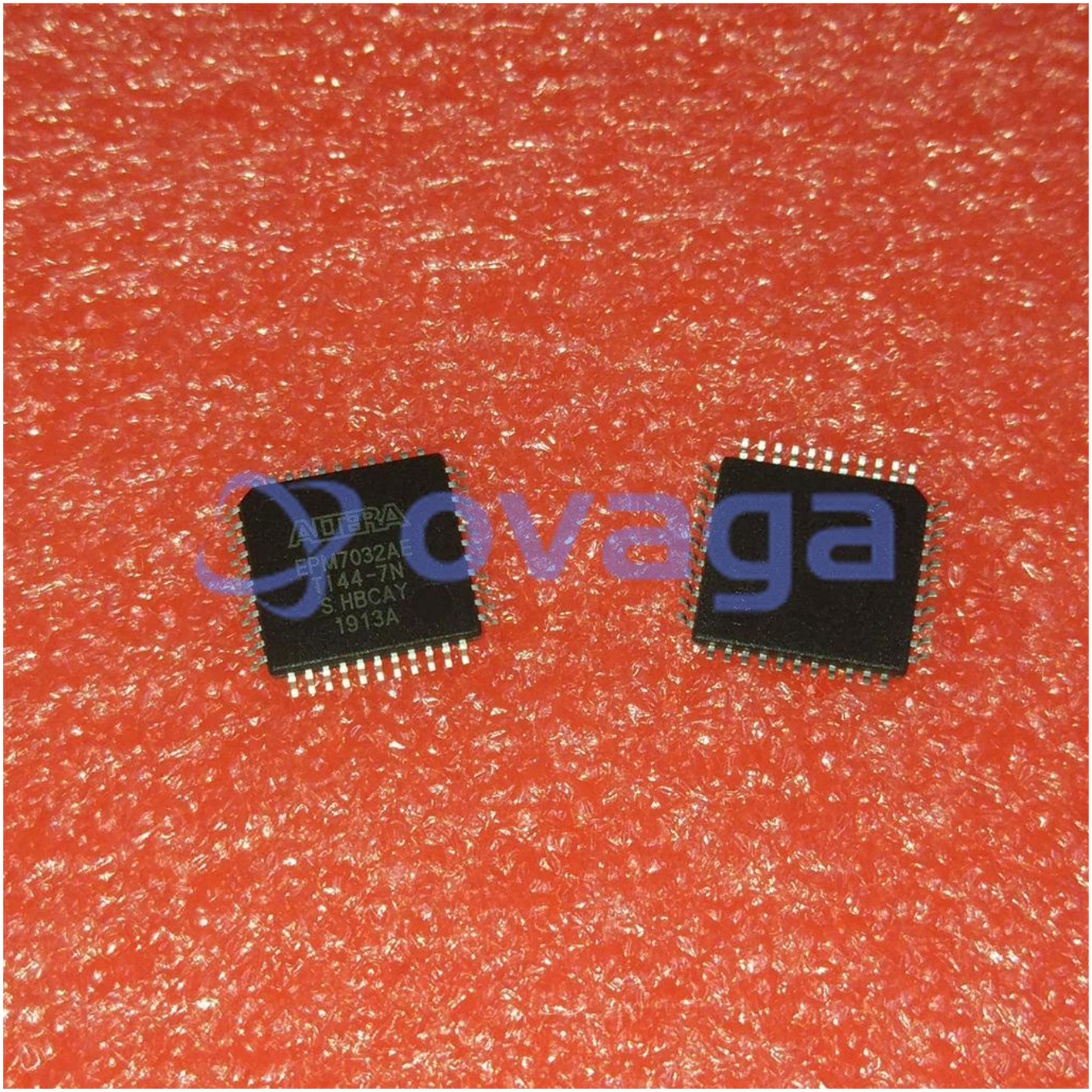
It has 1,000 erase/program cycles and a data retention period of 20 years.

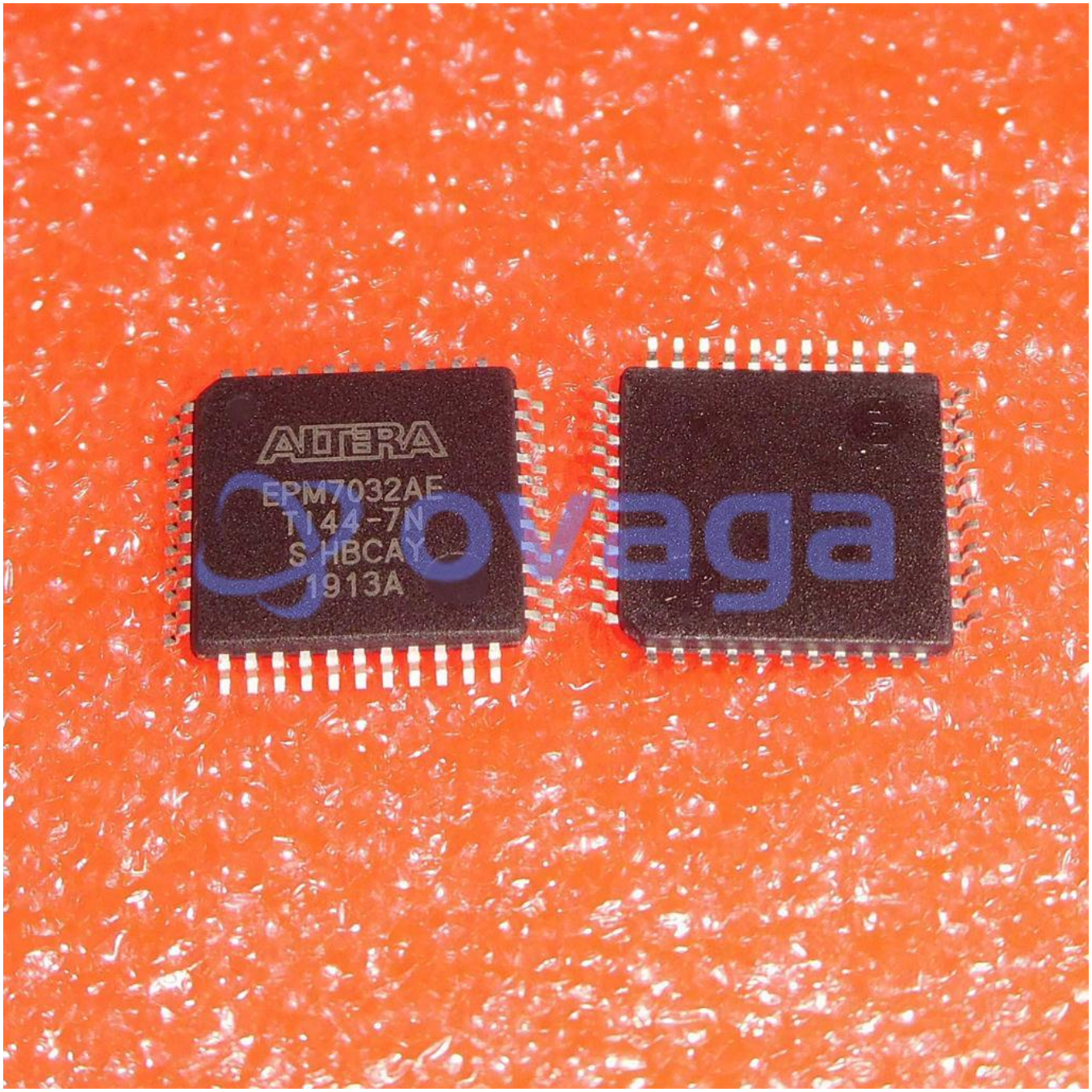
It is based on the EEPROM (Electrically Erasable Programmable Read-Only Memory) technology, which allows the device to be programmed and reprogrammed multiple times.

Application

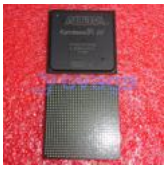
EPM7032AETI44-7N is commonly used in various digital circuits such as control logic, address decoding, and state machines.

It can be used in a wide range of applications including telecommunications, automotive, consumer electronics, medical devices, and industrial control systems.


ALTERA
EPM7032AE
T144-7N
S HBCAY
1913A



Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)

FBGA-780

[EPM240M100C5N](#)

Altera Corporation (Intel)

BGA-100

[EPM1270T144A5N](#)

Altera Corporation (Intel)

TQFP-144

[EPM570F256C5N](#)

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FBGA-256

EP2C35F672C8N

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FBGA-672

EPM7128AETC100-10

Altera Corporation (Intel)

TQFP-100

EP2C35F484C7N

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FBGA-484

EP2C35F484I8N

Altera Corporation (Intel)

FBGA-484