



Data Sheet

Ethernet Controller, 200 Mbps, IEEE 802.3, 3 V, 3.6 V, QFN, 56 Pins

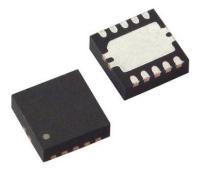
Manufacturers Microchip Technology, Inc

Package/Case VQFN-56

Product Type Interface ICs

RoHS Rohs

Lifecycle



Images are for reference only

Please submit RFQ for LAN9303-ABZJ or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFO

General Description

Microchip's LAN9303/LAN9303i is a high-performance, small-footprint, full-featured 3-port managed Ethernet switch. This device is application-optimized for consumer, and embedded electronics designs which have a rapid development cycle and require low-cost switching functionality, flexibility and ease of integration.

Featuring the industry's smallest form factor 3-port switch, the LAN9303/9303i supports compact PCB design and MII/RMII/Turbo MII (Media Independent, Reduced Media Independent and Turbo Media Independent Interfaces). This device supports Turbo MII mode to accomplish network speeds up to 200 Mbps. The virtual PHY feature mimics a single PHY connection through the MII/RMII/Turbo MII interface to the host SoC, which can reduce software development time. This feature also allows system designers to swap the single PHY and Ethernet switch to create multiple SKUs from the same software driver. Additionally, the LAN9303/9303M offer the industry's highest $(\pm 8kV/15kV)$ per-port ESD protection.

The LAN9303/9303 is witch combines all of the functions of a 10/100 system, including the switch fabric, packet buffers, buffer management, MACs, PHY transceivers and optional serial management functions. Both devices comply with IEEE 802.3 10BASE-T and 100BASE-TX Ethernet protocol specifications, enabling compatibility with industry standard Ethernet and Fast Ethernet applications.

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Features

Highlights

High-performance, full-featured 3-port switches with per-port IEEE 802.1Q VLAN support (up to 16 VLAN groups)

Industry's highest ($\pm 8kV/15kV$) per-port ESD protection

IGMP v1/v2 monitoring for multicast packet filtering

Up to 200 Mbps network speed via a Turbo MII interface

Optional EEPROM or external CPU serial management support via an I2C™ interface

Virtual PHY feature simplifies software development by mimicking multiple switch ports as a single-port PHY

Integrated regulators require a single 3.3V supply

42 per-port MIB counters for efficient network management

Industrial temperature (-40° to 85°C) options available (LAN9303i/9303Mi)

Package options:

LAN9303: 8x8mm, 56-pin QFN package for single MII/RMII/Turbo MII

Target Applications

Cable, Satellite, and IP Set-top Boxes (STBs)

Digital Televisions (DTVs)

Digital/Personal Video Recorders (DVRs/PVRs)

VoIP/Video Phone Systems

Home Gateways/Consumer Audio/Video

Test/Measurement Equipment

Industrial Automation and Control

LAN9303/9303M Application Examples

Related Products



LAN8740A-EN

Microchip Technology, Inc

QFN-32



LAN8700C-AEZG-TR

Microchip Technology, Inc

VQFN-36



LAN8700C-AEZG

Microchip Technology, Inc

VQFN-36



Microchip Technology, Inc VQFN-36

LAN8700IC-AEZG-TR



LAN9303I-ABZJ
Microchip Technology, Inc
VQFN-56



LAN9500AI-ABZJ-TR

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LAN9500A-ABZJ-TR



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LAN9500A-ABZJ