

EPM7256SQC208-10

Data Sheet

CPLD MAX 7000S Family 5K Gates 256 Macro Cells 100MHz 5V

Manufacturers <u>Altera Corporation (Intel)</u>

Package/Case PQFP-208

Product Type Programmable Logic ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for EPM7256SQC208-10 or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFO

General Description

EPM7256SQC208-10 is a programmable logic device (PLD) manufactured by Intel Corporation (now part of Altera Corporation). It belongs to the MAX 7000S family of devices and has 256 macrocells. The device is based on EEPROM technology and has a 10ns maximum propagation delay.

| Features | Application |
|---------------------------------|-------------------|
| 256 macrocells | XC95108-10PQ160C |
| 10ns maximum propagation delay | ATF1508AS-10AU100 |
| EEPROM technology | EPF10K10QC208-4 |
| 208-pin quad flat package (QFP) | LCMXO256C-3T100C |





Related Products



EP4CE55F29C8N

Altera Corporation (Intel) FBGA-780



EPM1270T144A5N

Altera Corporation (Intel) TQFP-144



EPM240M100C5N

Altera Corporation (Intel) BGA-100



EPM570F256C5N

Altera Corporation (Intel) FBGA-256



EP2C35F672C8N

Altera Corporation (Intel)
FBGA-672



Altera Corporation (Intel)
TQFP-100

EPM7128AETC100-10



EP2C35F484C7N
Altera Corporation (Intel)
FBGA-484



EP2C35F484I8N
Altera Corporation (Intel)
FBGA-484