

## XC95216 In-System Programmable CPLD

Manufacturers	<a href="#">AMD Xilinx, Inc</a>
Package/Case	QFP160
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC95216-20PQ160I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

RFQ

## General Description

XC95216-20PQ160I is a field programmable gate array (FPGA) chip from the Xilinx CoolRunner-II family. It has 216 macrocells, 48 input/output (I/O) pins, and operates at a maximum frequency of 125 MHz. The "20" in its part number indicates its speed grade, which means it can operate at a maximum frequency of 20 MHz, while the "PQ160" indicates its package type, which is a plastic quad flat pack with 160 pins.

## Features

- 216 logic cells that can be configured to perform various logic functions
- 48 I/O pins that can be used for input or output
- Programmable input and output slew rate control to minimize signal noise
- Built-in clock management resources, such as a delay-locked loop (DLL) and phase-locked loop (PLL)
- Built-in JTAG boundary scan testing for debugging and testing purposes
- Low power consumption

## Application

- Digital signal processing (DSP) and data processing
- Communications systems, such as wireless base stations and networking equipment
- Industrial automation and control systems
- Military and aerospace applications
- Automotive systems

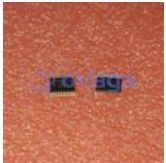


## Related Products



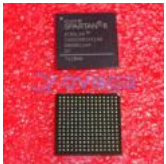
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AMD Xilinx, Inc  
SOP-20



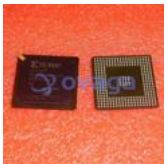
### [XCF04SV0G20C](#)

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TSSOP20



### [XC6SLX4-2CSG225C](#)

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BGA-225



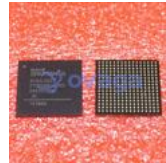
### [XCV50-6BG256C](#)

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BGA256



### [XCF08PV0G48C](#)

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### [XC6SLX25-3FTG256C](#)

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