

IC CLK FANOUT/BUFF ZD 28SSOP; HT SUSA CODE:8542390000

Manufacturers	<a href="#">Renesas Technology Corp</a>
Package/Case	28-SSOP (0.209, 5.30mm Width)
Product Type	Clock & Timer ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for 9DB106BFILF or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The 9DB106 zero-delay buffer supports PCIe Gen1 and Gen2 clocking requirements. The 9DB106 is driven by a differential SRC output pair from an IDT CK410/CK505-compliant main clock generator. It attenuates jitter on the input clock and has a selectable PLL bandwidth to maximize performance in systems with or without Spread-Spectrum clocking. An SMBus interface allows control of the PLL bandwidth and bypass options, while 2 clock request (CLKREQ#) pins make the 9DB106 suitable for Express Card applications.

## Features

6 - 0.7 V current mode differential output pairs (HCSL)

CLKREQ# pin for outputs 1 and 4/ supports Express Card applications

PLL or bypass mode/PLL can dejitter incoming clock

Selectable PLL bandwidth/minimizes jitter peaking in downstream PLL's

Spread Spectrum Compatible/tracks spreading input clock for low EMI

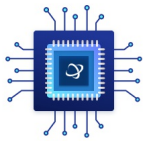
SMBus Interface/unused outputs can be disabled

Cycle-to-cycle jitter < 50 ps

Output-to-output skew < 50 ps

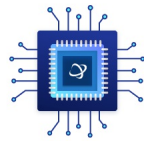


### Related Products



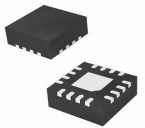
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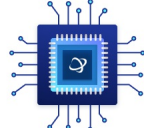
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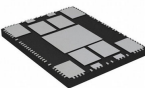
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24-VFQFN



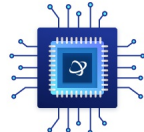
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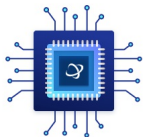
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VFQFPN-40



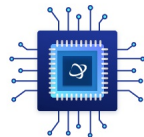
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