

EPM7128AETI100-7

Data Sheet

CPLD MAX® 7000A Family 2.5K Gates 128 Macro Cells 129.9MHz 3.3V 100-Pin TQFP Tray

Manufacturers	Altera Corporation (Intel)
Package/Case	TQFP-100
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFC	Q for EPM7128AETI100-7 or <u>Email to us: sales@ovaga.com</u> We will contact yo	ou in 12 hours	<u>RFQ</u>
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General Description

EPM7128AETI100-7 is a specific model of a programmable logic device (PLD) produced by Intel (formerly Altera). Here are its features:

Features

It belongs to the MAX 7000 series of PLDs.

It has a capacity of 128 macrocells, which can be configured as either combinatorial or sequential Lattice Semiconductor's LC4032V-10TN44C logic elements.

It has a maximum operating frequency of 167 MHz.

It uses the EPM7128SLC84-7 package and operates with a 3.3V power supply.

Application

XC9536XL-10VQG44C from Xilinx

ATF1504ASV-15QC100 from Microchip Technology.





Related Products



EP4CE55F29C8N Altera Corporation (Intel) FBGA-780



EPM1270T144A5N
Altera Corporation (Intel)
TQFP-144







EP2C35F484C7N Altera Corporation (Intel) FBGA-484











Altera Corporation (Intel) FBGA-484

EPM240M100C5N Altera Corporation (In

Altera Corporation (Intel) BGA-100

EPM570F256C5N

Altera Corporation (Intel) FBGA-256

EPM7128AETC100-10

Altera Corporation (Intel) TQFP-100

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