

# **70V09L20PFGI**

**RFO** 

Data Sheet

#### IC SRAM 1MBIT 20NS 100TQFP; HT SUSA CODE:8542320040

Manufacturers

Renesas Technology Corp

Package/Case

TQFP-100

Product Type

Memory

RoHS

Rohs

Lifecycle

Images are for reference only

## **General Description**

The 70V09 is a high-speed 128K x 8 Dual-Port Static RAM designed to be used as a stand-alone 1024K-bit Dual-Port SRAM or as a combination MASTER/SLAVE Dual-Port SRAM for 16-bit-or-more word systems which results in full speed, error-free operation without the need for additional discrete logic. An automatic power down feature controlled by the chip enables (either CE0 or CE1) permit the on-chip circuitry of each port to enter a very low standby power mode.

Please submit RFQ for 70V09L20PFGI or Email to us: sales@ovaga.com We will contact you in 12 hours.

### **Features**

True Dual-Ported memory cells which allow simultaneous reads of the same memory location

70V09 easily expands data bus width to 16 bits or more

Dual chip enables allow for depth expansion without external logic

Busy and Interrupt Flag

On-chip port arbitration logic

Full on-chip hardware support of semaphore signaling between ports

Fully asynchronous operation from either port

TTL-compatible, single  $3.3V (\pm 0.3V)$  power supply

Available in 100-pin TQFP packages

Industrial temperature range (-40C to +85C) is available

#### **Related Products**



70V261L25PFGI

Renesas Technology Corp LQFP-100



X28HC64SIZ-70

Renesas Technology Corp SOIC28



**70V28L20PFGI** 

Renesas Technology Corp TQFP-100



#### **70V27L15PFG**

Renesas Technology Corp TQFP-100



#### 70V24L15PFG

Renesas Technology Corp 100TQFP



#### **71256S70DB**

Renesas Technology Corp 28-CDIP (0.600, 15.24mm)



#### **70V28L15PFG**

Renesas Technology Corp TQFP-100



#### 70V261L25PFG

Renesas Technology Corp TQFP-100