

MAX® 9000 Programmable Logic Device (PLD) Family; 208 pin QFP; 0 to 90°C



Images are for reference only

Manufacturers	Altera Corporation (Intel)
Package/Case	QFP-208
Product Type	Programmable Logic ICs
RoHS	Rohs
Lifecycle	

Please submit RFQ for EPM9560ARC208-10N or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

EPM9560ARC208-10N is a product code for a field-programmable gate array (FPGA) manufactured by Intel (formerly known as Altera).

Features

It has 9,660 logic elements (LEs) and 252 embedded memory blocks (MBs)

Operating voltage: 2.5V

Maximum frequency: 250 MHz

Package: 208-pin FineLine BGA

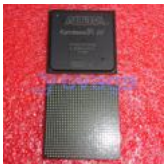
Temperature range: -40°C to +100°C

Application

EPM9560ARC208-10N FPGA can be used in various applications such as digital signal processing (DSP), system-on-chip (SoC) integration, embedded processing, and other high-performance applications that require large-scale integration and high-speed data transfer.



Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)
FBGA-780



[EPM1270T144A5N](#)

Altera Corporation (Intel)
TQFP-144



[EP2C35F672C8N](#)

Altera Corporation (Intel)
FBGA-672



[EP2C35F484C7N](#)

Altera Corporation (Intel)
FBGA-484



[EPM240M100C5N](#)

Altera Corporation (Intel)
BGA-100



[EPM570F256C5N](#)

Altera Corporation (Intel)
FBGA-256



[EPM7128AETC100-10](#)

Altera Corporation (Intel)
TQFP-100



[EP2C35F484I8N](#)

Altera Corporation (Intel)
FBGA-484