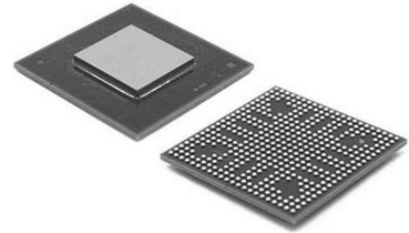


LDO Regulator Pos 1.5V to 5.1V 0.8A 8-Pin LFCSP EP T/R

Manufacturers	<a href="#">Analog Devices, Inc</a>
Package/Case	LFCSP-8
Product Type	Power Management ICs
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for ADM7151ACPZ-04-R7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The ADM7151 is a low dropout (LDO) linear regulator that operates from 4.5 V to 16 V and provides up to 800 mA of output current. Using an advanced proprietary architecture, it provides high power supply rejection (>90 dB from 1 kHz to 1 MHz), ultralow noise (1.7 nV/√Hz from 10 kHz to 1 MHz), and excellent line and load transient response with a 10 μF ceramic output capacitor. The output voltage can be set to any voltage between 1.5 V and 5.1 V with two resistors.

The ADM7151 is available in two models that optimize power dissipation and PSRR performance as a function of input and output voltage. See Table 6 and Table 7 for selection guides.

The ADM7151 regulator output noise is 1.0 μV rms from 100 Hz to 100 kHz, and the noise spectral density is 1.7 nV/√Hz from 10 kHz to 1 MHz.

The ADM7151 is available in 8-lead, 3 mm × 3 mm LFCSP and 8-lead SOIC packages, making it not only a very compact solution, but also providing excellent thermal performance for applications requiring up to 800 mA of output current in a small, low profile footprint.

ADM7151 is an adjustable V<sub>out</sub> device. For fixed V<sub>out</sub> version of the ADM7151, see the ADM7150.

## Features

Input voltage range: 4.5 V to 16 V

Maximum output current: 800 mA

Adjustable output from 1.5 V to 5.1 V

Low noise

1.0  $\mu$ V rms total integrated noise from 100 Hz to 100 kHz

1.6  $\mu$ V rms total integrated noise from 10 Hz to 100 kHz

Noise spectral density: 1.7 nV $\sqrt$ Hz from 10 kHz to 1 MHz

Power supply rejection ratio (PSRR) at 400 mA load

Dropout voltage: 0.6 V at >

Initial voltage accuracy:  $\pm$ 1%

Voltage accuracy over line, load and temperature:  $\pm$ 2%

Quiescent current (IGND): 4.3 mA at no load

Low shutdown current: 0.1  $\mu$ A

Stable with a 10  $\mu$ F ceramic output capacitor

8-lead LFCSP package and 8-lead SOIC package

## Application

Regulated power noise sensitive applications

RF mixers, phase-locked loops (PLLs), voltage-controlled oscillators (VCOs), and PLLs with integrated VCOs

Clock distribution circuits

Ultrasound and other imaging applications

High speed RF transceivers

High speed, 16-bit or greater ADCs

Communications and infrastructure

Cable digital-to-analog converter (DAC) drivers

## Related Products



[ADP3336ARMZ-REEL7](#)

Analog Devices, Inc  
MSOP-8



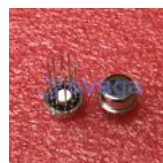
[AD737JRZ](#)

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Analog Devices, Inc  
SOIC-8



[AD636JH](#)

Analog Devices, Inc  
TO-100-10



[ADP3330ARTZ.3-RL7](#)

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