

Analog to Digital Converters - ADC High Spd Lo Pwr 4-CH Simult Sampling 12B

|               |                                     |
|---------------|-------------------------------------|
| Manufacturers | <a href="#">Analog Devices, Inc</a> |
| Package/Case  | QFP-44                              |
| Product Type  | Data Conversion ICs                 |
| RoHS          | Rohs                                |
| Lifecycle     |                                     |



Images are for reference only

Please submit RFQ for AD7864BSZ-1 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

Any subset of the four channels can be converted to maximize the throughput rate on the selected sequence. Select the channels to convert via hardware (channel select input pins) or software (programming the channel select register).

A single conversion start signal (CONVST) simultaneously places all the track-and-holds into hold and initiates a conversion sequence for the selected channels. The EOC signal indicates the end of each individual conversion in the selected conversion sequence. The BUSY signal indicates the end of the conversion sequence.

Data is read from the part by a 12-bit parallel data bus using the standard CS and RD signals. Maximum throughput for a single channel is 500 kSPS. For all four channels, the maximum throughput is 130 kSPS for the read-during-conversion sequence operation. The throughput rate for the read-after-conversion sequence operation depends on the read cycle time of the processor. See the Timing and Control section. The AD7864 is available in a small (0.3 square inch area) 44-lead MQFP.

## Features

High speed (1.65  $\mu$ s) 12-bit ADC

4 simultaneously sampled inputs

4 track-and-hold amplifiers 0.35  $\mu$ s track-and-hold acquisition time 1.65  $\mu$ s conversion time per channel

HW/SW select of channel sequence for conversion

Single-supply operation

Selection of input ranges  $\pm 10$  V,  $\pm 5$  V for AD7864-1  $\pm 2.5$  V for AD7864-3 0 V to 2.5 V, 0 V to 5 V for AD7864-2

High speed parallel interface that allows interfacing to 3 V processors

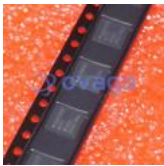
See data sheet for additional features



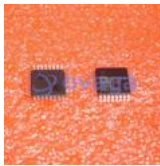




## Related Products



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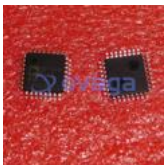
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