

SMD VERSION HI4-0546 HT SUSA CODE:8542390000

Manufacturers	<a href="#">Renesas Technology Corp</a>
Package/Case	CLCC28
Product Type	Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 5962-85131013A or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The HI-546/883 and HI-547/883 are analog multiplexers with active overvoltage protection and guaranteed rON matching. Analog input levels may greatly exceed either power supply without damaging the device or disturbing the signal path of other channels. Active protection circuitry assures that signal fidelity is maintained even under fault conditions that would destroy other multiplexers. Analog inputs can withstand constant 70VP-P levels with  $\pm 15V$  supplies. Digital inputs will also sustain continuous faults up to 4V greater than either supply. In addition, signal sources are protected from short circuiting should multiplexer supply loss occur. Each input presents 1k $\Omega$  of resistance under this condition. These features make the HI-546/883 and HI-547/883 ideal for use in systems where the analog inputs originate from external equipment or separately powered circuitry. Both devices are fabricated with 44V dielectrically isolated CMOS technology. The HI-546/883 is a single 16-channel, and the HI-547/883 is an 8-channel differential version. If input overvoltage protection is not needed, the HI-506/883 and HI-507/883 multiplexers are recommended. For further information see application note AN520.

## Features

This circuit is processed in accordance to MIL-STD-883 and is fully conformant under the provisions of Paragraph 1.2.1.

No channel interaction during overvoltage

Guaranteed  $r_{ON}$  matching

44V maximum power supply

Break-before-make switching

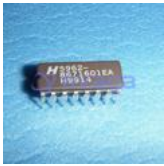
Analog signal range:  $\pm 15V$

Access time (max):  $1.0\mu s$

Power dissipation (max): 45mW



## Related Products



[5962-8671601EA](#)

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CDIP16



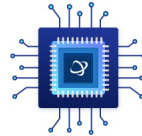
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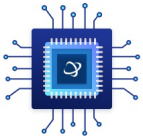
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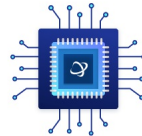
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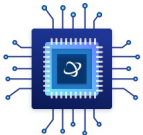
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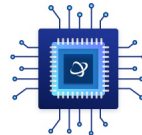
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