

# XC3S5000-5FG900C

Data Sheet

FPGA Spartan®-3 Family 5M Gates 74880 Cells 725MHz 90nm Technology 1.2V 900-Pin F-BGA

Manufacturers	AMD Xilinx, Inc
Package/Case	BGA-900
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC3S5000-5FG900C or Email to us: sales@ovaga.com We will contact you in 12 hours.

#### <u>RFO</u>

## **General Description**

XC3S5000-5FG900C is a Field-Programmable Gate Array (FPGA) manufactured by Xilinx. It is a member of the Spartan-3 family of FPGAs and is designed for high-performance digital signal processing and embedded processing applications.

Features	Application
Logic Cells: 5 million	XC3S5000-4FG900C
Block RAM: 18.4 Mb	XC3S5000-5FG1156C
Digital Clock Manager (DCM) Blocks: 24	XC3S5000-4FGG900C
Maximum Internal Clock Frequency: 500 MHz	XC3S5000-5FTG256I
I/O Pins: 636	XC3S5000-5FGG456C
Operating Voltage: 1.2V	XC3S5000-5FTG256C



#### **Related Products**



XC18V01S020C AMD Xilinx, Inc SOP-20



## XCF04SV0G20C

AMD Xilinx, Inc TSSOP20



XC6SLX4-2CSG225C AMD Xilinx, Inc BGA-225



## XCV50-6BG256C AMD Xilinx, Inc

AMD Xılınx, Inc BGA256



### XCF08PV0G48C

AMD Xilinx, Inc TSOP-48

#### XC6SLX25-3FTG256C

AMD Xilinx, Inc BGA-256

#### XC6SLX16-3CSG324C

AMD Xilinx, Inc BGA-324

## XCF32PVO48C

AMD Xilinx, Inc TSOP48