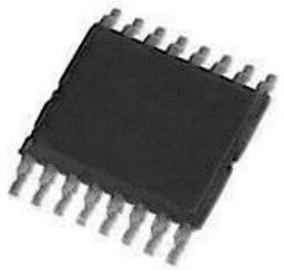
ADG1436YRUZ

Data Sheet

Analogue Switch, Dual Channel, 2 Channels, SPDT, 1.5 ohm, ± 4.5V to ± 16.5V, TSSOP, 16 Pins

Manufacturers	Analog Devices, Inc
Package/Case	TSSOP-16
Product Type	Analog Switch ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADG1436YRUZ or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

The ADG1436 is a monolithic CMOS device containing twoindependently selectable SPDT switches. An EN input on theLFCSP package enables or disable the device. When disabled, allchannels are switched off. Each switch conducts equally well inboth directions when on and has an input signal range thatextends to the supplies. In the off condition, signal levels up tothe supplies are blocked. Both switches exhibit break-before-makeswitching action for use in multiplexer applications.

The ADG1436 is designed on an iCMOS® process. iCMOS(industrial-CMOS) is a modular manufacturing process combininghigh voltage CMOS (complementary metal-oxide semiconductor)and bipolar technologies. It enables the development of a widerange of high performance analog ICs capable of 33 V operationin a footprint that no previous generation of high voltage partshas been able to achieve. Unlike analog ICs using conventionalCMOS processes, iCMOS components can tolerate high supplyvoltages while providing increased performance, dramaticallylower power consumption, and reduced package size.

The on-resistance profile is very flat over the full analog inputrange, ensuring excellent linearity and low distortion whenswitching audio signals. iCMOS construction ensures ultralowpower dissipation, making the part ideally suited for portableand battery-powered instruments.

Product Highlights

2.6 Ω maximum on resistance over temperature.

Minimum distortion.

Ultralow power dissipation: <0.03 µW.

16-lead TSSOP and 16-lead 4 mm × 4 mm LFCSP packages.

Features

1.5 Ω on resistance

0.3 Ω on-resistance flatness

0.1 Ω on-resistance match between channels

Continuous current per channel

LFCSP package: up to 400 mA

TSSOP package: up to 260 mA

Fully specified at +12 V, ±15 V, and ±5 V

No VL supply required

3 V logic-compatible inputs

Rail-to-rail operation

16-lead TSSOP and 4 mm × 4 mm, 16-lead LFCSP packages

ADG1436-EP supports defense and aerospace applications (AQEC standard)

Download

Military temperature range (−55°C to +125°C)

1 assembly/test site

Product change notification

Qualification data available on request

Application

Automatic test equipment

Data acquisition systems

Battery-powered systems

Sample-and-hold systems

Audio signal routing

Communication systems

Relay replacement

Related Products

ADV7181CBSTZ

Analog Devices, Inc

LQFP-64

AD8170AR

Analog Devices, Inc

SOP8

AD724JR

Analog Devices, Inc

SOIC-16

ADV7393BCPZ

Analog Devices, Inc

LFCSP-VQ-40

ADV7391WBCPZ

Analog Devices, Inc

LFSCP-3

ADV7390BCPZ

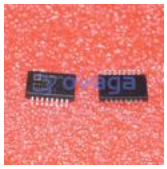
Analog Devices, Inc

QFN32

ADV7341BSTZ

Analog Devices, Inc

LQFP-64



ADUM4160BRIZ

Analog Devices, Inc

SOIC-16