

SSM2143SZ

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Data Sheet

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Manufacturers	Analog Devices, Inc	
Package/Case	SOIC-8	The second second
Product Type	Amplifier ICs	
RoHS	Rohs	
Lifecycle		Images are for reference only

Please submit RFQ for SSM2143SZ or Email to us: sales@ovaga.com We will contact you in 12 hours.

-6 dB Differential Line Receiver; Package: SOIC; No of Pins: 8; Temperature Range: TBD

<u>RFQ</u>

General Description

Additional features of the device include a skew rate of 10 V/ μ s and wide bandwidth. Total harmonic distortion (THD) is less than 0.004% over the full audio band, even while driving low impedance loads. The SSM2143 input stage is designed to handle input signals as large as +28 dBu at = 1/2 applications, a gain of 2 can be realized by reversing the +IN/-IN and SENSE/REFERENCE connections.

When configured for a gain of 1/2, the SSM2143 and SSM2142 Balanced Line Driver provide a fully integrated, unity gain solution to driving audio signals over long cable runs. For similar performance with>

Features

High Common-Mode RejectionDC: 90 dB typ60 Hz: 90 dB typ20 kHz: 85 dB typ

Ultralow THD: 0.0006% typ @ 1 kHz

Fast Slew Rate: 10 V/ms typ

Wide Bandwidth: 7 MHz typ>

Two Gain Levels Available>

Low Cost





Related Products



Analog Devices, Inc SOP8

<u>SSM2211SZ</u>



SOP8 <u>SSM2135SZ</u>

Analog Devices, Inc SOIC-8





<u>SSM2164S</u>

Analog Devices, Inc SOP-16

SSM3302ACPZ

Analog Devices, Inc LFCSP-40



<u>SSM2142PZ</u>

Analog Devices, Inc DIP-8



<u>SSM2165-1S</u>

Analog Devices, Inc SOP8



<u>SSM2135S</u>

Analog Devices, Inc SOIC-8



<u>SSM2143P</u>

Analog Devices, Inc DIP-8