

Complete, 12-Bit A/D Converters with Microprocessor Interface; Temperature Range: See Datasheet; Package: 28-PDIP

Manufacturers	<a href="#">Renesas Technology Corp</a>
Package/Case	PDIP-28
Product Type	Data Conversion ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for HI3-574AJN-5Z or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The HI-X74(A) is a complete 12-bit, Analog-to-Digital Converter, including a +10V reference clock, three-state outputs and a digital interface for microprocessor control. Successive approximation conversion is performed by two monolithic dice housed in a 28 lead package. The bipolar analog die features the Intersil Dielectric Isolation process, which provides enhanced AC performance and freedom from latch-up. Custom design of each IC (bipolar analog and CMOS digital) has yielded improved performance over existing versions of this converter. The voltage comparator features high PSRR plus a high speed current-mode latch, and provides precise decisions down to 0.1 LSB of input overdrive. More than 2X reduction in noise has been achieved by using current instead of voltage for transmission of all signals between the analog and digital ICs. Also, the clock oscillator is current controlled for excellent stability over temperature. The HI-X74(A) offers standard unipolar and bipolar input ranges, laser trimmed for specified linearity, gain and offset accuracy. The low noise buried zener reference circuit is trimmed for minimum temperature coefficient. Power requirements are +5V and  $\pm 12V$  to  $\pm 15V$ , with typical dissipation of 385mW (HI-574A/674A) at 12V.

## Features

Complete 12-Bit A/D Converter with Reference and Clock

Full 8-Bit, 12-Bit or 16-Bit Microprocessor Bus Interface

Bus Access Time 150ns

No Missing Codes Over Temperature

Minimal Setup Time for Control Signals

Fast Conversion Times

HI-574A (Max) 25 $\mu$ s

HI-674A (Max) 15 $\mu$ s

Low Noise, via Current-Mode Signal Transmission Between Chips

Byte Enable/Short Cycle (AO Input)

Guaranteed Break-Before-Make Action, Eliminating Bus Contention During Read Operation. Latched by Start Convert Input (To Set the Conversion Length)

Supply Voltage  $\pm 12V$  to  $\pm 15V$

Pb-Free Available (RoHS Compliant)



## Related Products



### [HI1-574AJD-5](#)

Renesas Technology Corp  
CDIP-28



### [HI1-574ATD-2](#)

Renesas Technology Corp  
DIP28



### [HI1-574AKD-5](#)

Renesas Technology Corp  
28-CDIP (0.600, 15.24mm)



### [HI1-574ASD-2](#)

Renesas Technology Corp  
28-CDIP (0.600, 15.24mm)



[HI3-574AKN-5Z](#)

Renesas Technology Corp  
PDIP-28



[HI1-565ATD-2](#)

Renesas Technology Corp  
DIP24



[HI5760BIBZ-T](#)

Renesas Technology Corp  
SOIC-28 Wide



[HI5760BIBZ](#)

Renesas Technology Corp  
SOIC-28 Wide