

CPLD MAX® II Family 192 Macro Cells 247.5MHz 0.18um Technology 2.5V/3.3V 100-Pin TFBGA Tray



Images are for reference only

Manufacturers	<u>Altera Corporation (Intel)</u>
Package/Case	FBGA-100
Product Type	Programmable Logic ICs
RoHS	Rohs
Lifecycle	

Please submit RFQ for EPM240F100C4N or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

EPM240F100C4N is an FPGA (Field-Programmable Gate Array) chip manufactured by Intel (formerly Altera).

Features

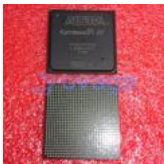
- 240 Logic elements (LEs)
- 6,624 bits of on-chip memory (RAM)
- 18 multipliers
- 16 input pins and 4 output pins
- 3.3V power supply
- Operating temperature range of -40°C to +85°C
- Available in a 100-pin TQFP package

Application

- Control systems
- Digital signal processing (DSP)
- Communications systems
- Test and measurement equipment
- Video and image processing
- Automotive and industrial control systems



Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)
FBGA-780



[EPM1270T144A5N](#)

Altera Corporation (Intel)
TQFP-144



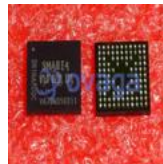
[EP2C35F672C8N](#)

Altera Corporation (Intel)
FBGA-672



[EP2C35F484C7N](#)

Altera Corporation (Intel)
FBGA-484



[EPM240M100C5N](#)

Altera Corporation (Intel)
BGA-100



[EPM570F256C5N](#)

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FBGA-256



[EPM7128AETC100-10](#)

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TQFP-100



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Altera Corporation (Intel)
FBGA-484