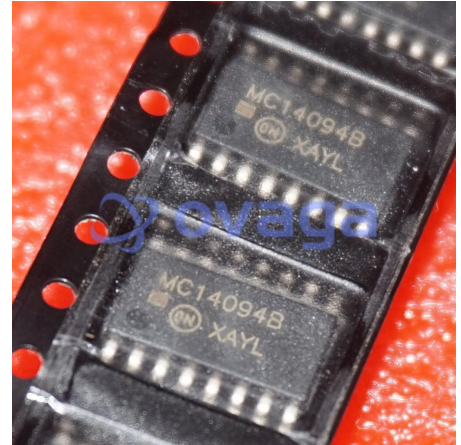


Shift Register/Latch Single 8-Bit Serial to Serial/Parallel Automotive 16-Pin SOIC T/R

Manufacturers	<u>ON Semiconductor, LLC</u>
Package/Case	SOIC-16
Product Type	Logic ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for MC14094BDR2G or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The MC14094B combines an 8-stage shift register with a data latch for each stage and a three-state output from each latch. Data is shifted on the positive clock transition and is shifted from the seventh stage to two serial outputs. The QS output data is for use in high-speed cascaded systems. The Q S output data is shifted on the following negative clock transition for use in low-speed cascaded systems. Data from each stage of the shift register is latched on the negative transition of the strobe input. Data propagates through the latch while strobe is high. Outputs of the eight data latches are controlled by three-state buffers which are placed in the high-impedance state by a logic Low on Output Enable.

## Features

Three-State Outputs

Capable of Driving Two Low-Power TTL Loads or One Low-Power Schottky TTL Load Over the Rated Temperature Range

Input Diode Protection

Data Latch

Dual Outputs for Data Out on Both Positive and Negative Clock Transitions

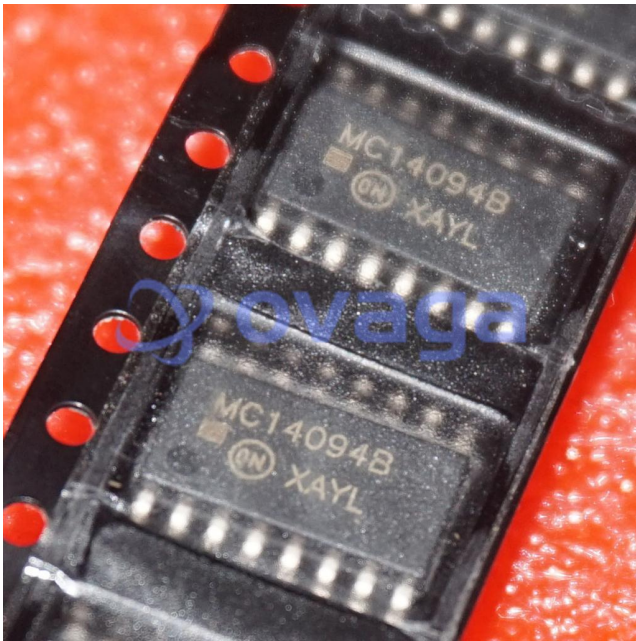
Useful for Serial-to-Parallel Data Conversion

Pin-for-Pin Compatible with CD4094B

Pb-Free Packages are Available\*

## Application

ONSEMI

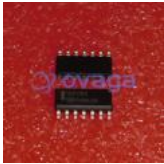


## Related Products



### [MC14013BDR2G](#)

ON Semiconductor, LLC  
SOIC-14



### [MC14011BDG](#)

ON Semiconductor, LLC  
SOIC-14



### [MC74VHC1G08DFT1G](#)

ON Semiconductor, LLC  
SC-70



### [MC100EP52MNR4G](#)

ON Semiconductor, LLC  
QFN-24



### [MC74VHCT50ADTR2G](#)

ON Semiconductor, LLC  
TSSOP-14



### [MC74VHC1G32DFT1G](#)

ON Semiconductor, LLC  
SC-70



### [MC74LCX16245DTG](#)

ON Semiconductor, LLC  
TSSOP-48



### [MC74AC14DTR2G](#)

ON Semiconductor, LLC  
TSSOP-14