

FPGA FLEX 10KA Family 100K Gates 4992 Cells 125MHz 0.3um Technology 3.3V 240-Pin RQFP

Manufacturers	Altera Corporation (Intel)
Package/Case	QFP-240
Product Type	Programmable Logic ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for EPF10K100ARC240-3N or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

EPF10K100ARC240-3N is a complex programmable logic device (CPLD) from the family of Altera (now Intel) FLEX 10K series. Here is some information about its features and applications:

Features

It has 10,000 logic elements (LEs) which are basic building blocks of CPLD.

It operates on a 3.3V power supply and has a maximum pin count of 240.

It has a maximum propagation delay of 5.5 nanoseconds.

It has an on-chip oscillator for generating a clock signal.

It has programmable macrocells, which can be configured as flip-flops, latches, or combinational logic.

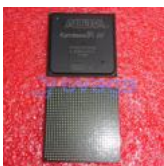
Application

EPF10K100ARC240-3N is used in various digital applications where custom logic is required. It is commonly used in industrial control systems, communication equipment, medical devices, and automotive systems.

It is also used in prototyping and testing of digital circuits due to its reprogrammability.

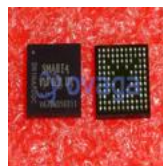


Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)
FBGA-780



[EPM240M100C5N](#)

Altera Corporation (Intel)
BGA-100



[EPM1270T144A5N](#)

Altera Corporation (Intel)
TQFP-144



[EPM570F256C5N](#)

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FBGA-256



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[EPM7128AETC100-10](#)

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TQFP-100



[EP2C35F484C7N](#)

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FBGA-484