

FPGA Spartan-II Family 600K Gates 15552 Cells 357MHz 0.15um Technology 1.8V 456Pin FBGA

Manufacturers	AMD Xilinx, Inc
Package/Case	BGA-456
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC2S600E-6FGG456C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

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General Description

XC2S600E-6FGG456C is a field-programmable gate array (FPGA) from the Xilinx Spartan-II family. Here's some information about its features, applications, and equivalent parts:

Features

The XC2S600E-6FGG456C has 600,000 system gates, which makes it a medium-sized FPGA.

It has 456 pins in the FGG456 package.

The device has 96 I/O pins that can be used as input or output.

It has 14 dedicated clock input pins and 5 global clock distribution networks.

The FPGA has a 2.5V power supply and can operate at a maximum frequency of 100 MHz.

It has 2,688 programmable logic cells, 30 block RAMs, and 28,672 bits of distributed RAM.

Application

The XC2S600E-6FGG456C can be used in a wide range of applications, including digital signal processing, video processing, communication systems, and industrial control systems.

It is also suitable for use in aerospace and defense applications due to its radiation tolerance.



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