

EPM7256SQC208-15N

Data Sheet

CPLD MAX® 7000S Family 5K Gates 256 Macro Cells 76.9MHz 5V 208-Pin PQFP Tray

Manufacturers <u>Altera Corporation (Intel)</u>

Package/Case PQFP-208

Product Type Programmable Logic ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for EPM7256SQC208-15N or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFO

General Description

EPM7256SQC208-15N is a programmable logic device (PLD) manufactured by Intel (formerly Altera). It belongs to the MAX 7000 series of PLDs and has a maximum gate count of 5,000 gates.

Features	Application
5,000 gate count	Digital signal processing
256 macrocells	Video processing
208-pin quad flat pack (QFP) package	Industrial control systems
15ns propagation delay	Automotive applications



-15N

Related Products



EP4CE55F29C8N

Altera Corporation (Intel) FBGA-780

BDE241343A



EPM1270T144A5N

Altera Corporation (Intel) TQFP-144



EPM240M100C5N

Altera Corporation (Intel) BGA-100



EPM570F256C5N

Altera Corporation (Intel) FBGA-256



EP2C35F672C8N

Altera Corporation (Intel)
FBGA-672



Altera Corporation (Intel) TQFP-100

EPM7128AETC100-10



EP2C35F484C7N
Altera Corporation (Intel)
FBGA-484



EP2C35F484I8N
Altera Corporation (Intel)
FBGA-484