

MC100LVEP111FAG

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Images are for reference only

Data Sheet

2.5V / 3.3V 1:10 Differential ECL/PECL/HSTL Clock Driver, Clock Drivers & Distribution 2.5V/3.3V 1:10 Diff ECL/PECL/HST Driver

Manufacturers	ON Semiconductor, LLC
1.1001100000000000000000000000000000000	011001100101,2220

Package/Case 32-LQFP

Product Type Clock & Timer ICs

Rohs

RoHS

Lifecycle

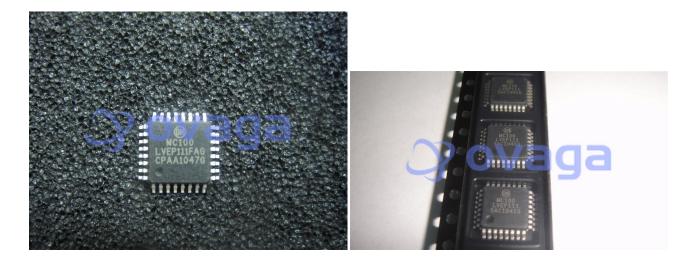
Please submit RFQ for MC100LVEP111FAG or Email to us: sales@ovaga.com We will contact you in 12 hours.

<u>RFQ</u>

General Description

The MC100LVEP111FAG is a differential 1:2 LVPECL fanout buffer IC designed to provide high-speed, low-skew distribution of clock or data signals. It accepts a single LVPECL input and generates two identical differential LVPECL outputs.

Features	Application
Differential 1:2 fanout buffer	Clock distribution in high-speed digital systems
LVPECL input compatibility	Data signal distribution in high-speed communication systems
High-speed operation	Timing and synchronization circuits
Low output skew	Test and measurement equipment
Wide operating voltage range	Networking equipment
Enable/disable control option	Data centers
	Telecommunications



Related Products



MC100EP809MNG

ON Semiconductor, LLC QFN-32



MC100EP14DTG ON Semiconductor, LLC TSSOP-20



MC100EP140DR2G ON Semiconductor, LLC SOIC-8



MC100LVE310FNR2G ON Semiconductor, LLC

PLCC-28L





MC1455BP1G ON Semiconductor, LLC DIP-8

TSSOP-20

MC100LVEP14DTR2G ON Semiconductor, LLC

MC100EP195FAR2G

ON Semiconductor, LLC LQFP-32

<u>MC1455P1G</u>



ON Semiconductor, LLC PDIP-8