

Single, Integer-N, 550 MHz PLL With Programmable Prescaler And Charge Pump; Package: TSSOP; No of Pins: 16; Temperature Range: Industrial

Manufacturers	<a href="#">Analog Devices, Inc</a>
Package/Case	TSSOP-16
Product Type	Clock/Timing - Clock Generators, PLLs, Frequency Synthesizers
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for ADF4110BRUZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The ADF4110 family of frequency synthesizers can be used to implement local oscillators in the up-conversion and down-conversion sections of wireless receivers and transmitters. They consist of low-noise digital PFD (Phase Frequency Detector), a precision charge pump, a programmable reference divider, programmable A and B counters and a dual modulus prescaler (P/P+1). The A (6-bit) and B (13-bit) counters, in conjunction with the dual modulus prescaler (P/P+1), implement an N divider.

Control of all the on-chip registers is via a simple 3-wire interface. The devices operate with a power supply ranging from 2.7 V to 5.5 V and can be powered down when not in use.

## Features

ADF4110: 550 MHz

ADF4111: 1.2 GHz

ADF4112: 3.0 GHz

ADF4113: 4.0 GHz

Separate V<sub>p</sub> Allows Extended Tuning Voltage in 3V Systems

Programmable Charge Pump Currents

Programmable Dual Modulus Prescaler 8/9, 16/17, 32/33, 64/65

Programmable Anti-Backlash Pulse Width

3-Wire Serial Interface

Digital Lock Detect

Power Down Mode



## Related Products



### [ADF4350BCPZ](#)

Analog Devices, Inc  
LFCSP-32



### [ADF4111BRUZ](#)

Analog Devices, Inc  
TSSOP-16



### [ADF4116BRUZ](#)

Analog Devices, Inc  
TSSOP-16



### [AD2S99BPZ](#)

Analog Devices, Inc  
PLCC-20



### [AD9516-4BCPZ](#)

Analog Devices, Inc  
LFCSP64



### [ADF4113BRUZ](#)

Analog Devices, Inc  
TSSOP-16



### [ADF4193BCPZ](#)

Analog Devices, Inc  
LFCSP-32



### [AD9528BCPZ](#)

Analog Devices, Inc  
72-VFQFN, CSP