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LAN8740A-EN

Data Sheet

<u>RFO</u>

Ethernet Controller, 100 Mbps, IEEE 802.3, IEEE 802.3u, 1.14 V, 3.6 V, QFN, 32 Pins

Manufacturers	Microchip Technology, Inc
Package/Case	QFN-32
Product Type	Interface ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for LAN8740A-EN or Email to us: sales@ovaga.com We will contact you in 12 hours.

General Description

Microchip's LAN8740A/LAN8740Ai are the latest generation of low-power, 10/100 Fast Ethernet PHYs featuring Energy Efficient Ethernet (IEEE 802.3az) and Wake-On-LAN functionality, which provide system sleep modes for improved power savings. The LAN8740A/LAN8740Ai feature deterministic loop back delay, assuring real-time system performance as well as cable diagnostics which reduce network installation costs. These devices are available in both commercial (0° to 70°C) and industrial (-40° to 85°C) temperature ranges. The LAN8740A/LAN8740Ai are well-suited for Consumer, Embedded and Industrial applications. Additionally, these devices support auto-negotiation, auto-polarity correction, HP Auto-MDIX† and are compliant with IEEE 802.3 and 802.3u standards. Microchip also offers its complimentary and confidential LANCheck® online design review services free of charge to customers who select our products for their application design-in.*

[†]HP Auto-MDIX eliminates the need for special "crossover" cables when connecting LAN devices together.

*The LANCheck online design review service is subject to Microchip's Program Terms and Conditions and requires a myMicrochip account.

Features



Related Products



LAN8700C-AEZG-TR Microchip Technology, Inc

VQFN-36



LAN8700IC-AEZG-TR Microchip Technology, Inc VQFN-36



LAN9303I-ABZJ Microchip Technology, Inc VQFN-56



LAN9500A-ABZJ-TR Microchip Technology, Inc

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LAN9303-ABZJ

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LAN8700C-AEZG

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LAN9500A-ABZJ



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