

EP1K30QC208-3

Data Sheet

FPGA Logic TC; Logic Type:Programmable; No. of Macrocells:147; Package/Case:208-PQFP; Leaded Process Compatible:No; Number of Circuits:1728; Peak Reflow Compatible (260 C):No; Mounting Type:surface mount RoHS Compliant: No PQFP, 208 Pin

Manufacturers Altera Corporation (Intel)

Package/Case QFP-208

Product Type Programmable Logic ICs

RoHS

Lifecycle



Images are for reference only

Please submit RFQ for EP1K30QC208-3 or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFO

General Description

EP1K30QC208-3 is a specific model of field-programmable gate array (FPGA) manufactured by Altera (now part of Intel).

Features Application

1,692 logic elements (LEs) Digital signal processing (DSP)

30,000 usable gates Embedded systems

288 kilobits (Kb) of embedded memory Video and image processing

52 embedded 18x18 multipliers High-speed communication systems

3.3V power supply Industrial control systems

208-pin quad flat pack (QFP) package Test and measurement equipment





Related Products



EP4CE55F29C8N

Altera Corporation (Intel) FBGA-780



EPM1270T144A5N

Altera Corporation (Intel) TQFP-144



EP2C35F672C8N

Altera Corporation (Intel) FBGA-672



EP2C35F484C7N

Altera Corporation (Intel) FBGA-484



EPM240M100C5N

Altera Corporation (Intel) BGA-100



EPM570F256C5N

Altera Corporation (Intel) FBGA-256



EPM7128AETC100-10

Altera Corporation (Intel) TQFP-100



EP2C35F484I8N

Altera Corporation (Intel) FBGA-484